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DATE	CHANGENO.	REV																																																																																														
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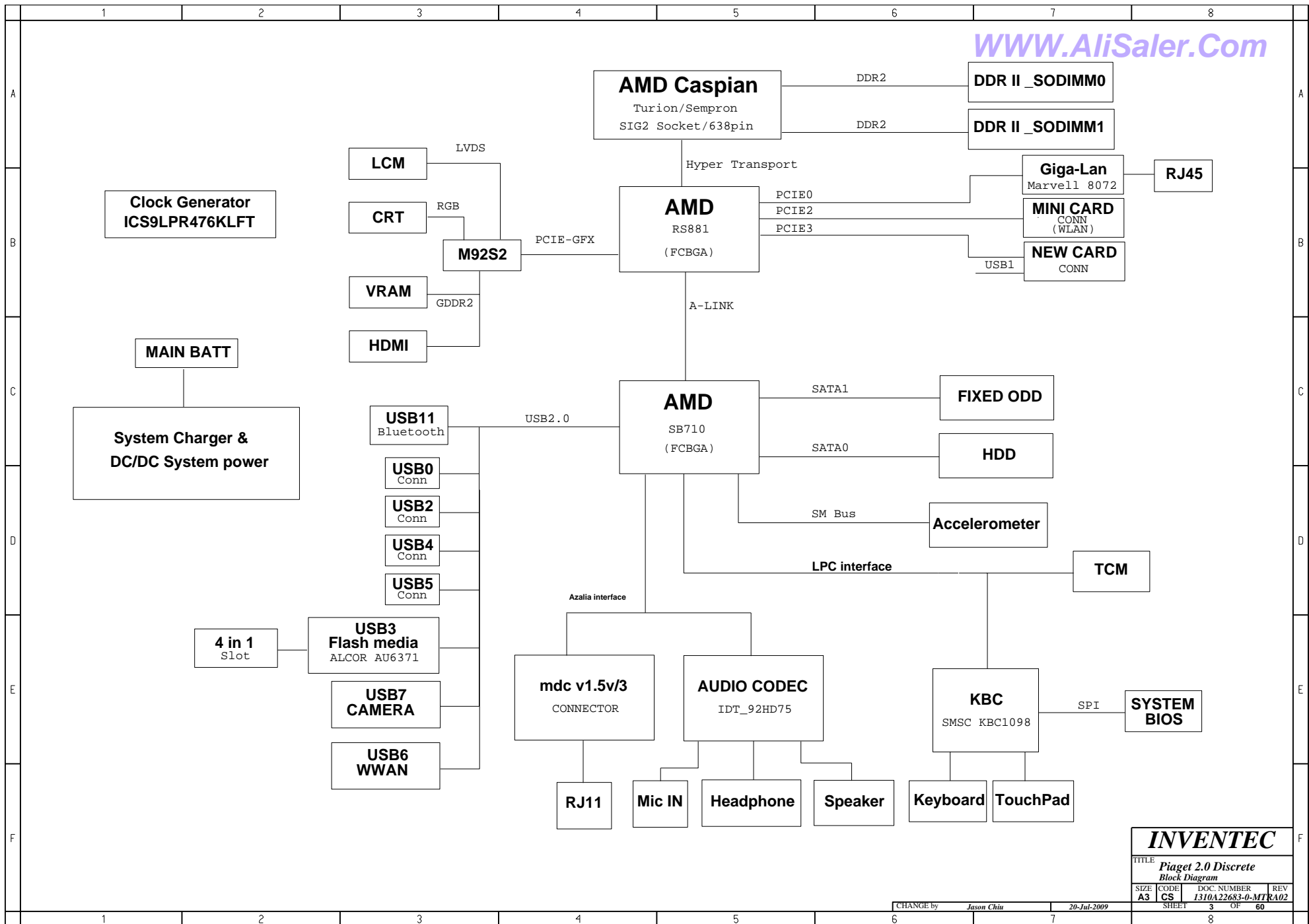
PAGE	PAGE	PAGE
5- DC& BATTERY CHARGER	15- CLOCK_GENERATOR	40- KBC
6- SELECT & BATTERY CONN	16- CPU-1	41- KB & TP CONN
7- +V5A,+V3A & +V2.5S	17- CPU-2	42- SPI & ACCELEROMETER
8- OCP	18- CPU-3	43- HDD & ODD CONN
9- +VCC_NB & +V1.2A	19- CPU-4	44- USB CONN
10- +VCC_CORE	20- DDR2-DIMM0	45- TCM CONN
11- POWER	21- DDR2-DIMM1	46- FLASH MEDIA CARD
12- DDR2 POWER	22- DDR2-DAMPING	47- AZALIA CODEC
13- POWER(SLEEP)	23- THERMAL&FAN CONTROLLER	48- Earphone & MIC JACK
14- POWER(SEQUENCE)	24- RS780-1	49- AUDIO AMP & HP JACK
	25- RS780-2	50- MDC CNTR
	26- RS780-3	51- Giga-LAN- CONTROLLER
	27- RS780-4	52- NIC-10/100/1000-RJ45 CONN
	28- GPU-AMD-M92S2-1	53- MINICARD & BT CONN
	29- GPU-AMD-M92S2-2	54- NEW CARD & SIM CONN
	29- GPU-AMD-M92S2-3	55- W/B & SW/B & LED/B CONN
	30- GPU-AMD-M92S2-4	56- SCREW
	32- Video RAM(GDDR2)-1	57- Power Plan CAP
	33- Video RAM(GDDR2)-2	58- HDMI
	34- CRT	59- 15" ODD Extend Board
	35- LCD CONN & WEBCAM CONN	60- Switch Board & LID Switch & QL
	36- SB700-1	
	37- SB700-2	
	38- SB700-3	
	39- SB700-4	

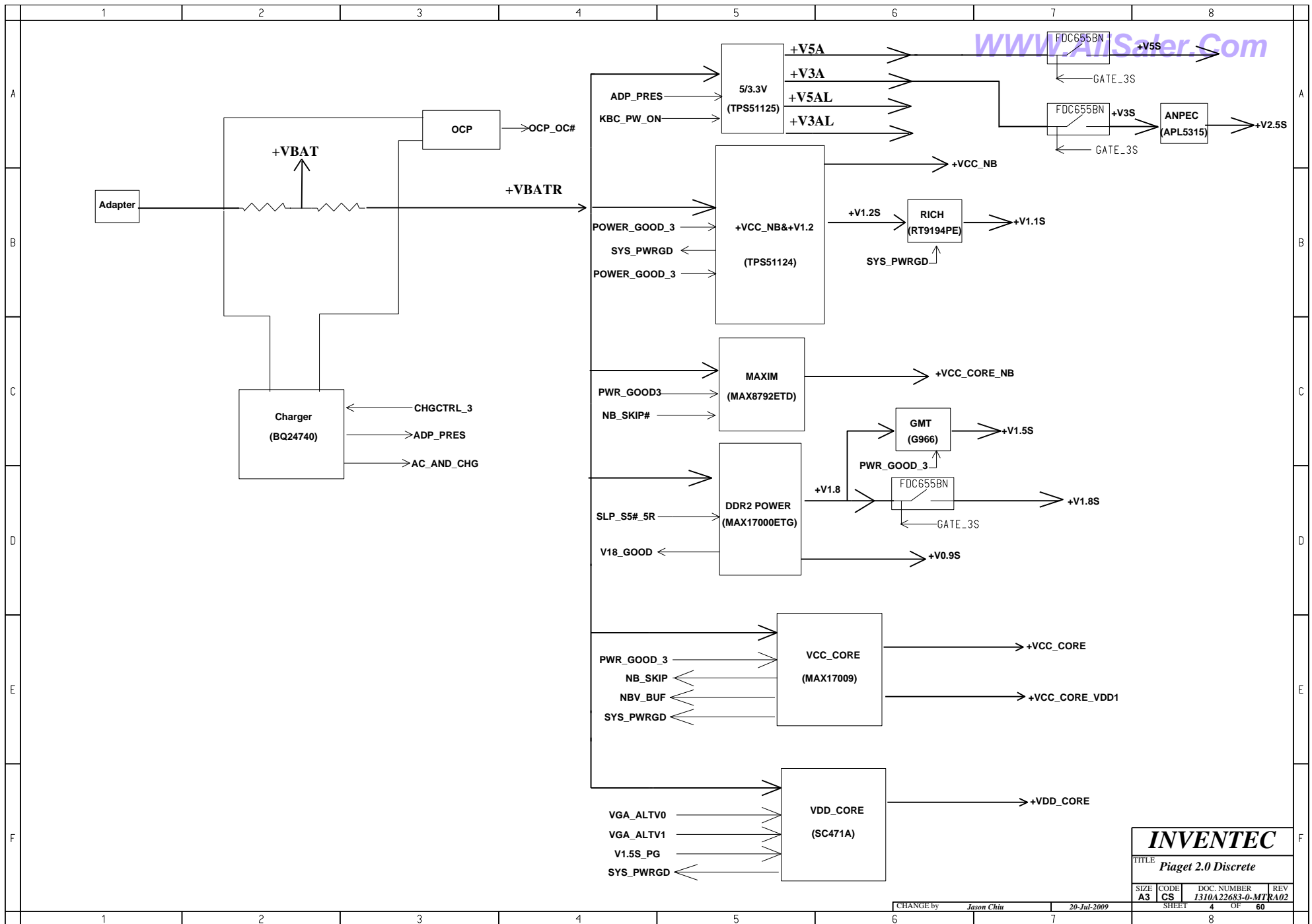
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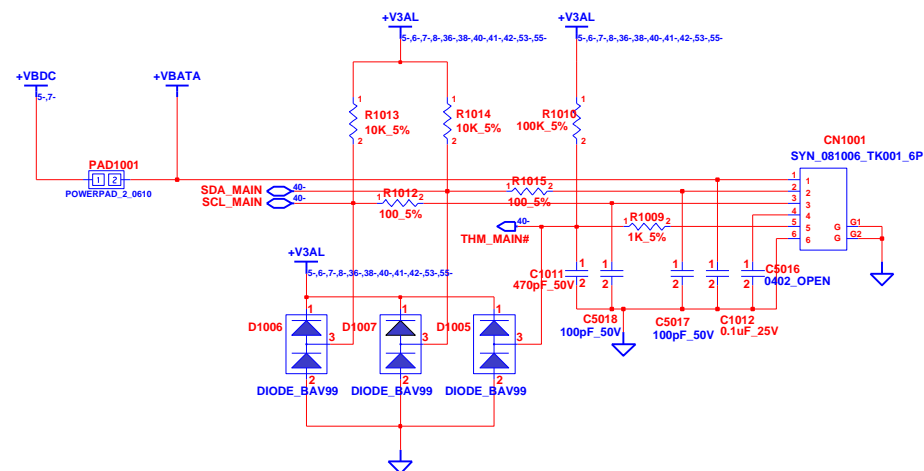
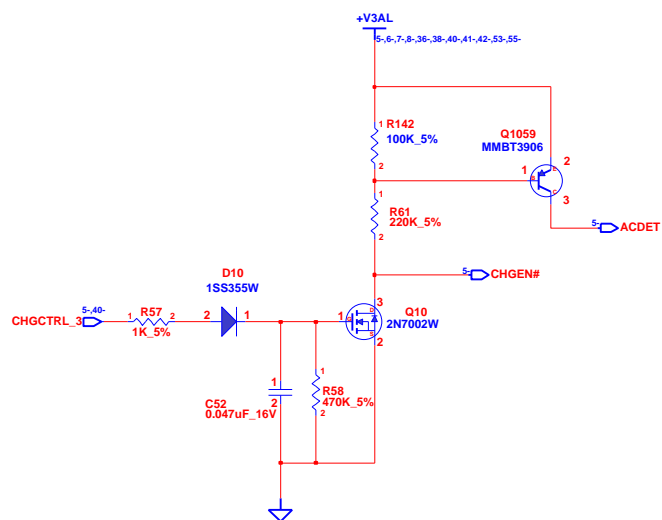
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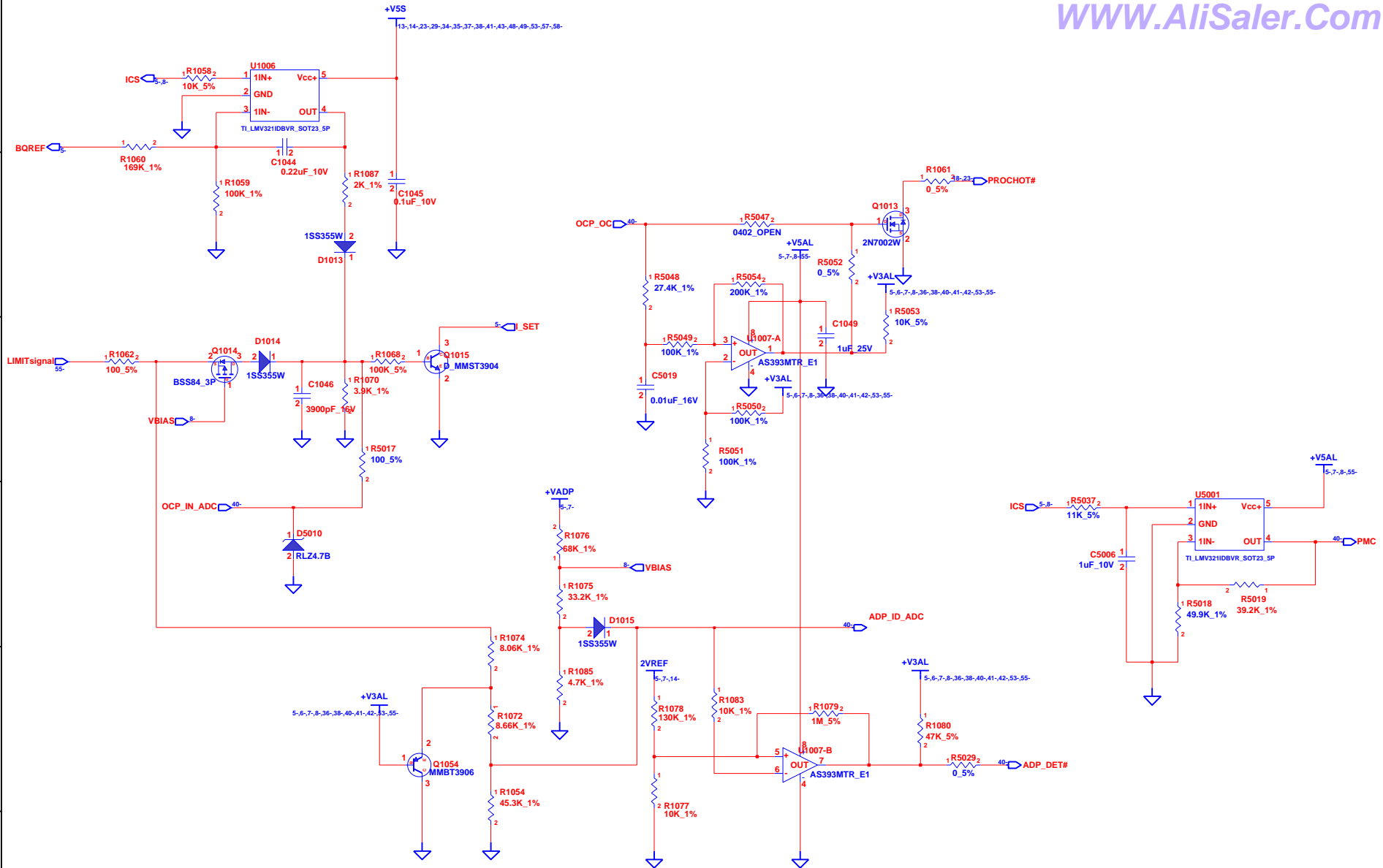
CHANGE by **Jason Chiu** 20-Jul-2009



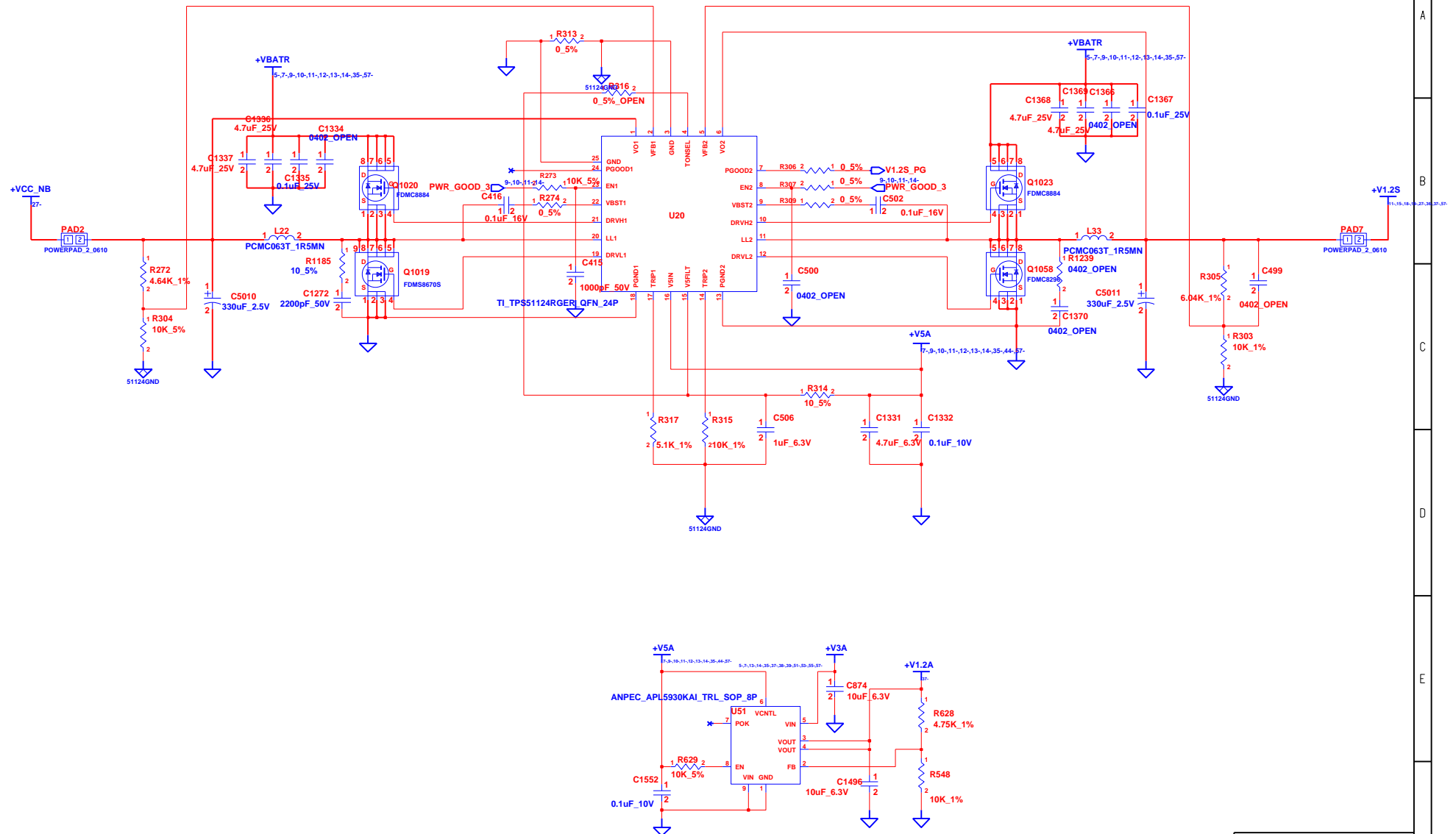




INVENTEC			
TITLE Piaget 2.0 Discrete SELECT & BATTERY CONN			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV RA02
CHANGE by <i>Jason Chiu</i>		20-Jul-2009	
SHEET		6	OF 60



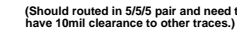
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SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
CHANGE by Jason Chiu		20-Jul-2009	SHEET 8 OF 60



INVENTEC

TITLE			
Piaget 2.0 Discrete			
+VCC_NB & +V1.2A			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22683-0-MTR	RA02

CHANGE by	Jason Chin	14-Aug-2009	SHEET	9	OF	60
				8		



TITLE			
Piaget 2.0 Discrete +VCC_CORE			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MTRA02	
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TITLE	<i>Piaget 2.0 Discrete POWER</i>
-------	----------------------------------

SIZE A3	CODE CS	DOC. NUMBER 1310A22683-0-MT	REV A02
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[illegible]

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NOTE: DDR2 REGULATOR

INVENTEC			
TITLE: Piaget 2.0 Discrete DDR2 POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22683-0-MTR	A02
CHANGE by		DATE	REV
Jason Chiu		20-Jul-2009	1
SHEET		OF	TOTAL
12		60	60

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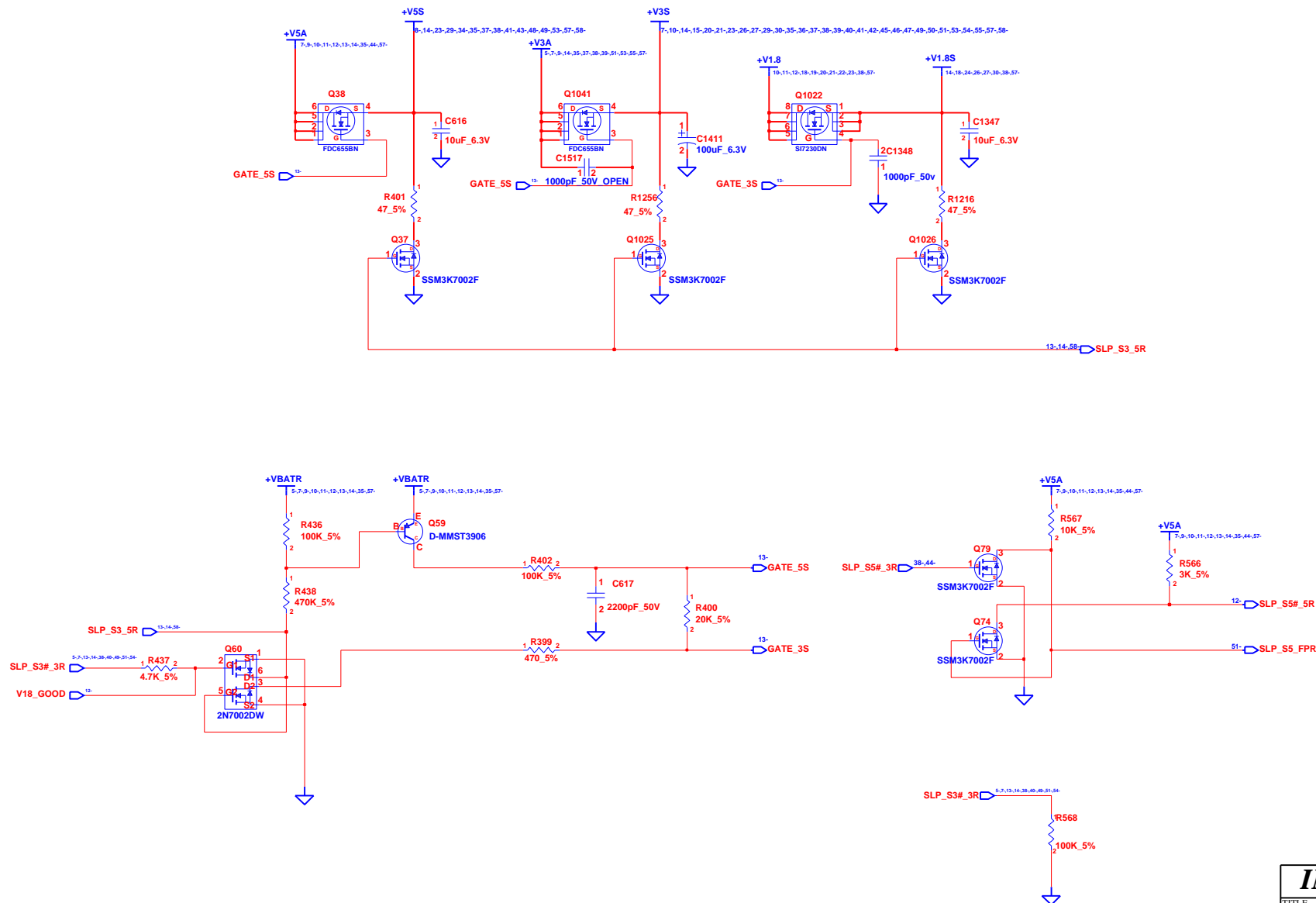
NOTE: DDR2 REGULATOR

INVENTEC			
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CHANGE by		DATE	SHEET
Jason Chiu		20-Jul-2009	12 OF 60

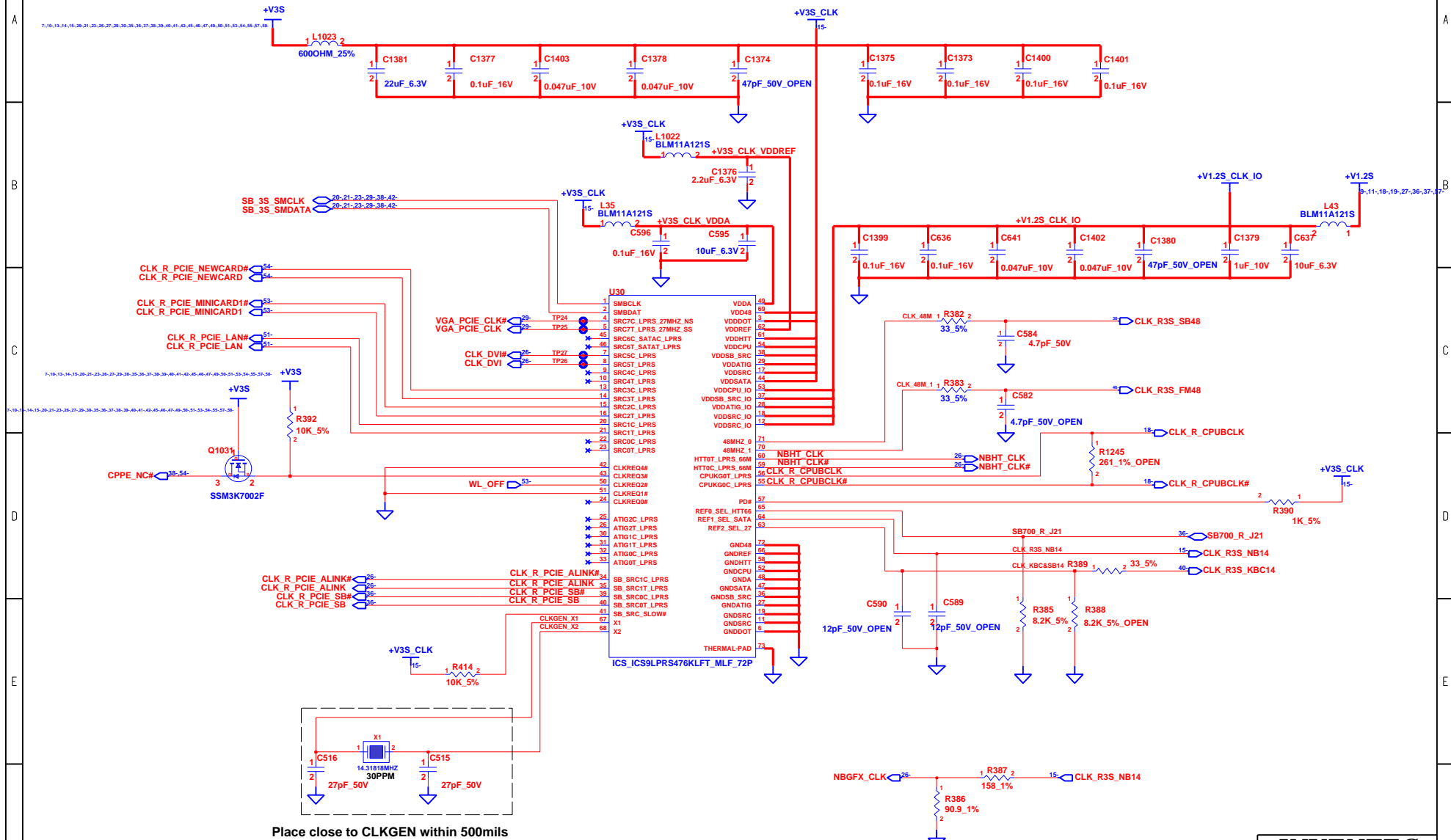
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INVENTEC			
TITLE: Piaget 2.0 Discrete POWER(Sleep)			
SIZE: A3	CODE: CS	DOC. NUMBER: I310A22683-0-MTR	REV: A02
CHANGE by: Jason Chiu		20-Jul-2009	
SHEET: 13		OF: 60	

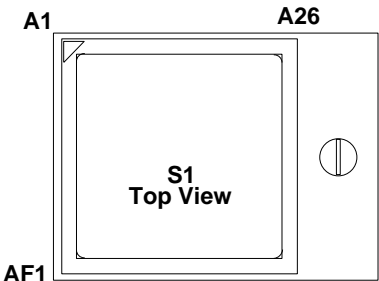
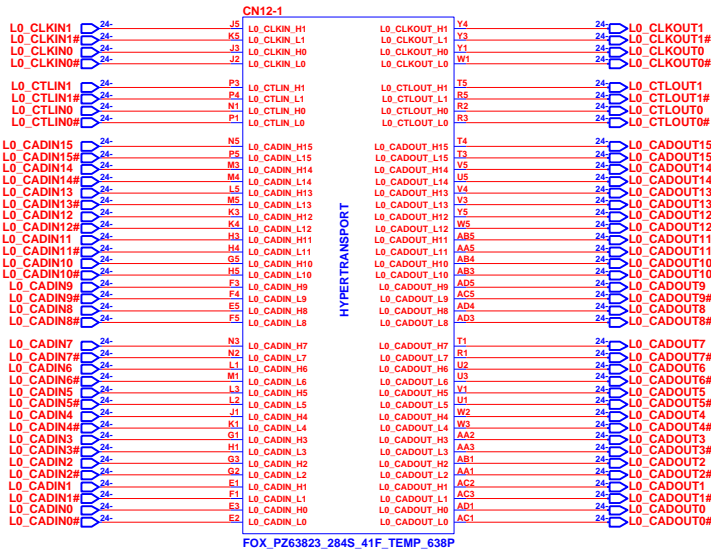


Place close to CLKGEN within 500mils

INVENTEC

TITLE			
Piaget 2.0 Discrete CLOCK GENERATOR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22683-0-MTR	A02
SHEET		15	OF 60

CHANGE by Jason Chiu 12-Aug-2009

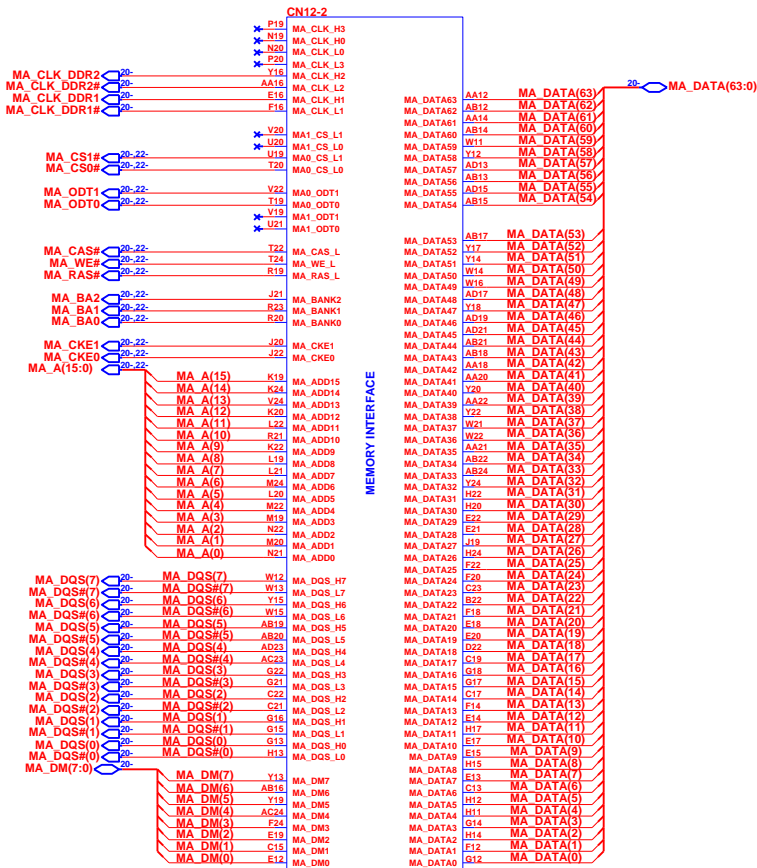


Layout: Add stitching caps if crossing plane split.

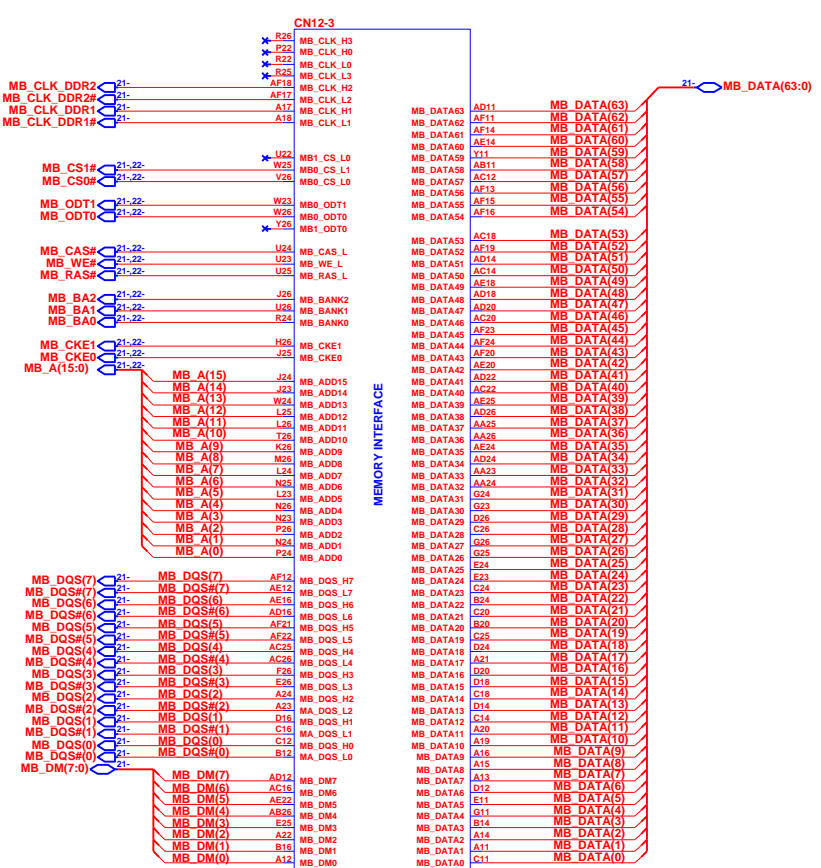
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TITLE Piaget 2.0 Discrete CPU-1			
SIZE A3	CODE CS	DOC. NUMBER 1310A22683-0-MTR	REV A02

CHANGE by Jason Chiu 20-Jul-2009

SHEET 16 OF 60



FOX_P263823_284S_41F_TEMP_638P

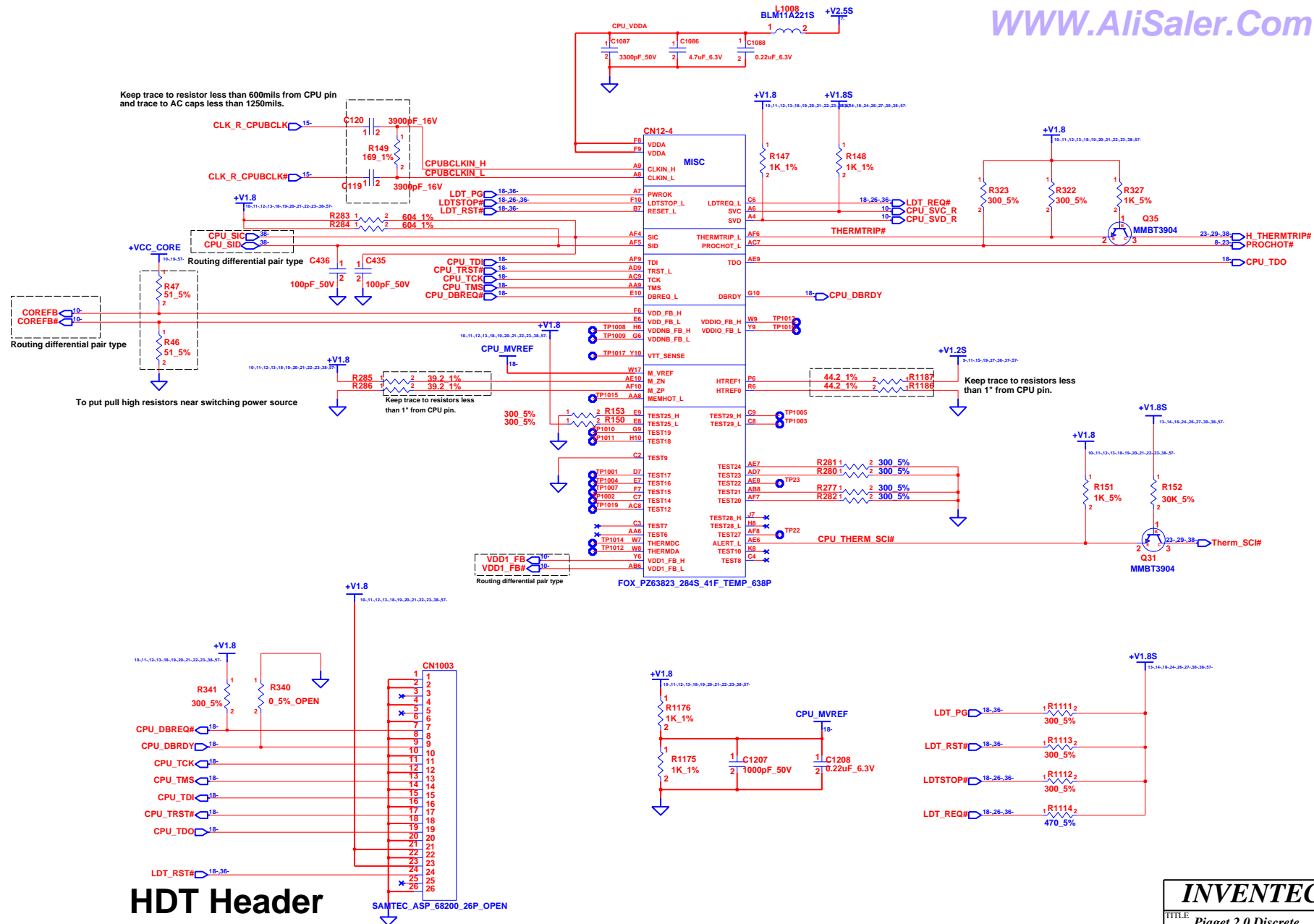


FOX_P263823_284S_41F_TEMP_638P

INVENTEC

TITLE Piaget 2.0 Discrete CPU-2			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
SHEET 17		OF 60	

CHANGE by Jason Chin 20-Jul-2009

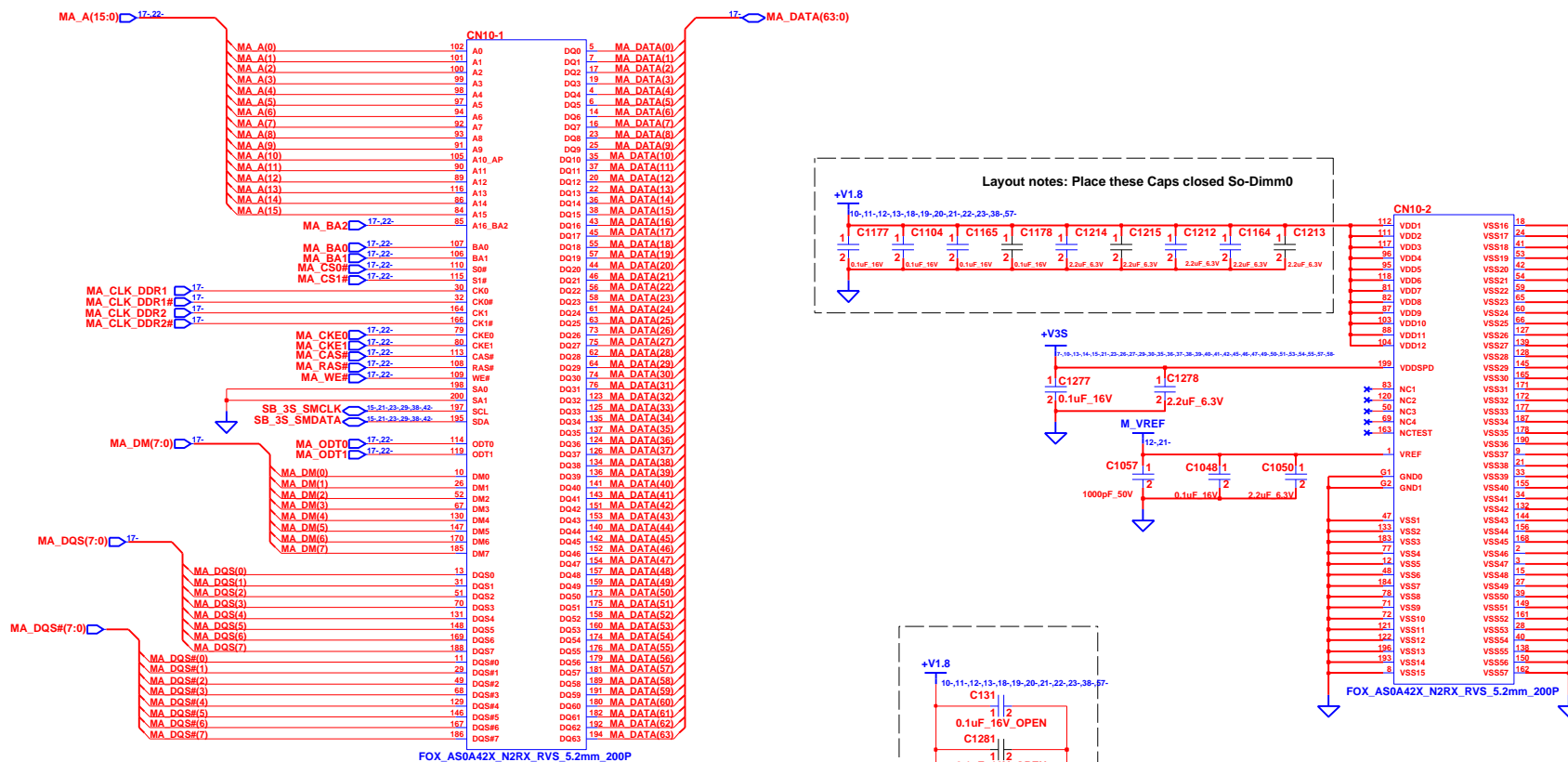


HDT Header

SAMTEC ASP 68200 26P OPEN

INVENTEC

TITLE			
<i>Piaget 2.0 Discrete CPU-3</i>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MTRA02	
SHEET		18 OF	60



SO DIMM_5.2mm

INVENTEC

TITLE Piaget 2.0 Discrete DDR2-DIMM-0			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR02	REV 1
CHANGE by Jason Chiu		20-Jul-2009	SHEET 20 OF 60

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Layout note: Place these Caps closed So-Dimm1

To CPU power sense

+V1.8

+V3S

M_VREF

FOX AS0A426_NARN_7F_200P

SODIMM_9.2mm

INVENTEC

TITLE
Piaget 2.0 Discrete
DDR2-DIMM-1

SIZE
A3

CODE
CS

DOC. NUMBER
1310A22683-0-MTR

REV
A02

CHANGE by Jason Chiu 20-Jul-2009

SHEET 21 OF 60

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The diagram illustrates the internal wiring of a DDR2-DIMM-1 module. It shows the connection between the module's pins (e.g., MB_A(0) to MB_A(15), MB_DATA(0) to MB_DATA(31), MB_DM(0) to MB_DM(7), MB_DQS(0) to MB_DQS(7)) and the internal components. Key components include capacitors (C106 to C117, C1279, C1280, C1051, C1052) and resistors (R10 to R58). Power management is detailed with +V1.8, +V3S, and M_VREF connections, including a note to place capacitors closed So-Dimm1. The module is identified as FOX_AS0A426_NARN_7F_200P.

SODIMM_9.2mm

INVENTEC

TITLE
Piaget 2.0 Discrete
DDR2-DIMM-1

SIZE
A3

CODE
CS

DOC. NUMBER
1310A22683-0-MTR

REV
A02

CHANGE by Jason Chiu 20-Jul-2009

SHEET 21 OF 60

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Layout note: Place these Caps closed So-Dimm1

To CPU power sense

FOX AS0A426_NARN_7F_200P

SODIMM_9.2mm

INVENTEC

TITLE
Piaget 2.0 Discrete
DDR2-DIMM-1

SIZE
A3

CODE
CS

DOC. NUMBER
1310A22683-0-MTR

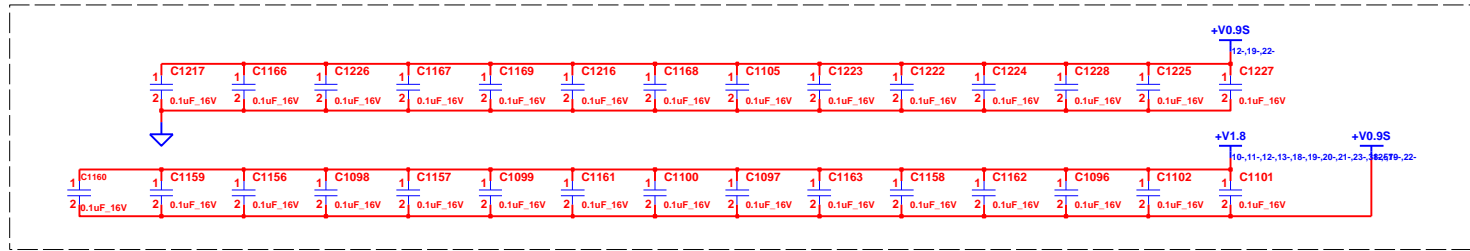
REV
A02

CHANGE by Jason Chiu 20-Jul-2009

SHEET 21 OF 60

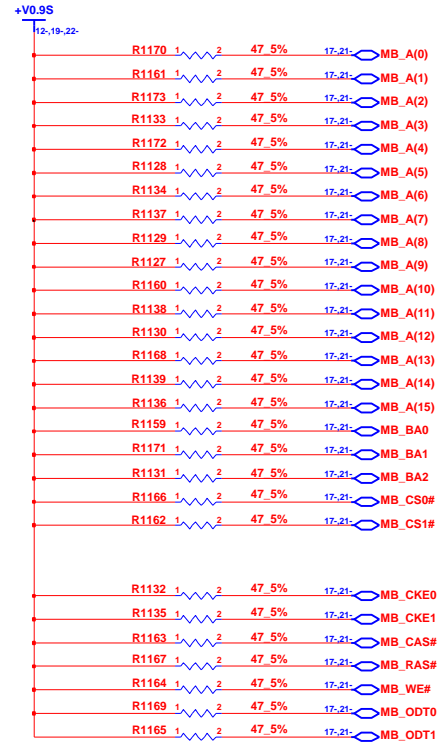
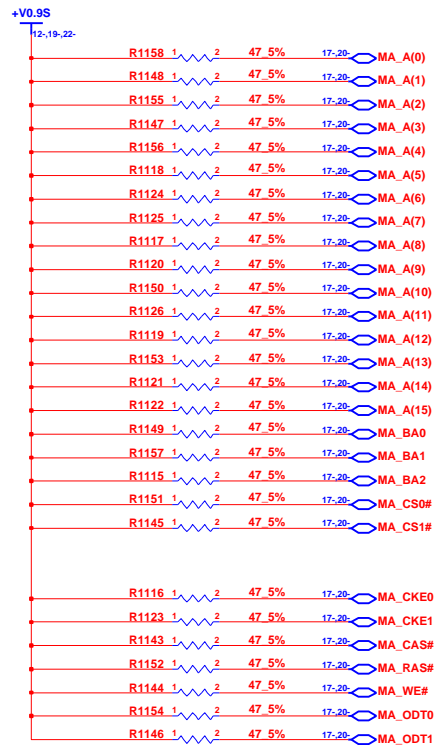
1 2 3 4 5 6 7 8

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To CPU power sense

Place CAPs close to DIMM



INVENTEC

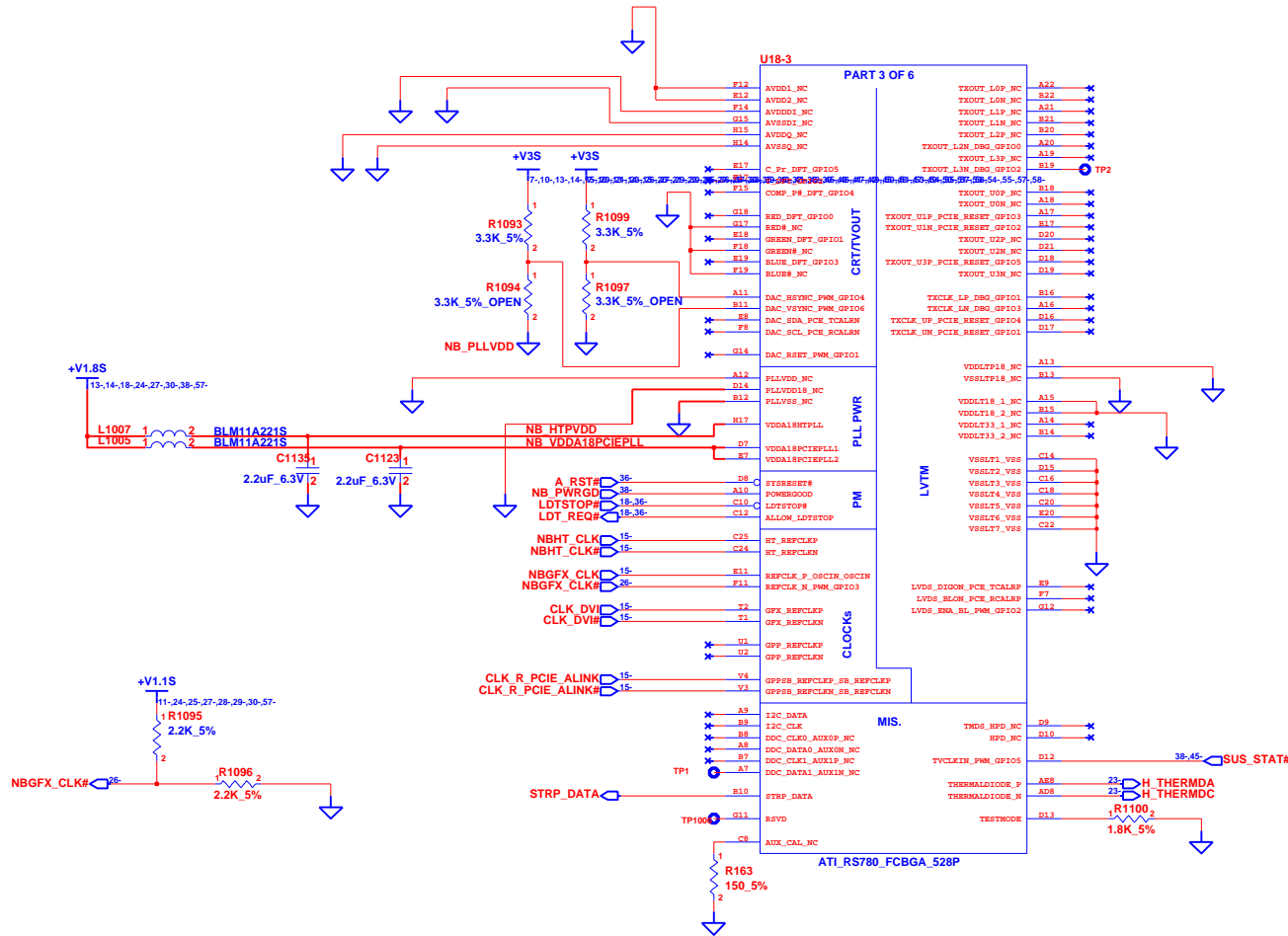
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Piaget 2.0 Discrete			
DDR2-DAMPING			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22683-0-MT	RA02
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CHANGE by Jason Chiu 20-Jul-2009

[illegible]

SHEET 23 OF 60

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INVENTEC

TITLE			
Piaget 2.0 Discrete RS780-3			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MTR	A02
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CHANGE by Jason Chiu 20-Jul-2009

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INVENTEC
Piaget 2.0 Discrete RS780-4

SIZE: A3 CODE: CS DOC. NUMBER: 1310A22683-0-MTR REV: A02

CHANGE by: Jason Chiu 20-Jul-2009 SHEET: 27 OF: 60

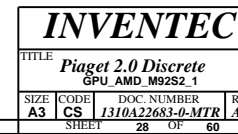
X = DESIGN DEPENDANT
0 = DO NOT INSTALL RESISTOR
1 = INSTALL 10K RESISTOR

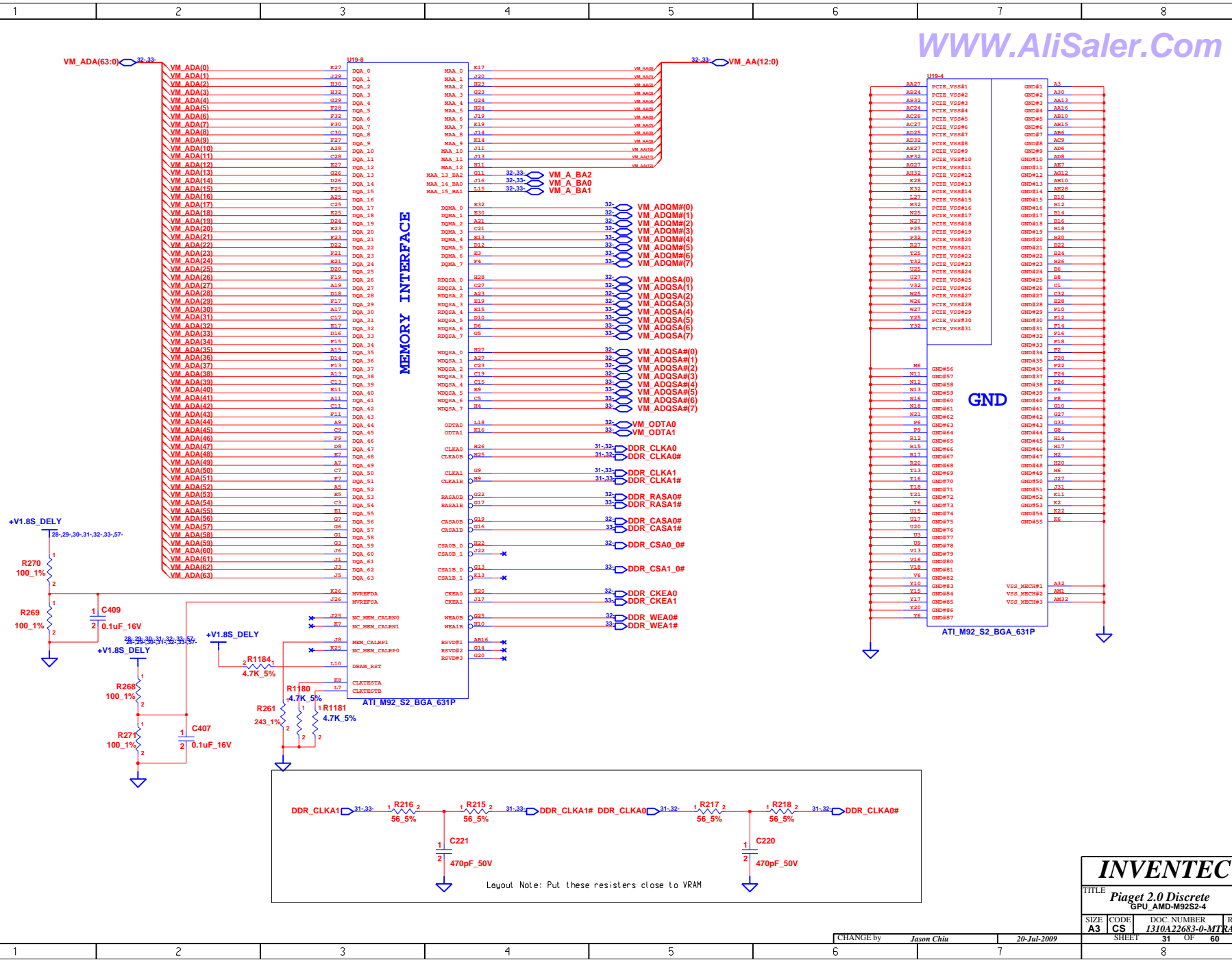
GPIO_13	GPIO_12	GPIO_11	For DIS-GDDR2
0	0	1	(512/256M) memory aperture
1	1	0	reserved

For DIS-GDDR2				
MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	vendor
0	0	1	1	hynix (512MB)
0	0	1	0	Qimonda(512M)
0	0	0	1	Samsung(512MB)

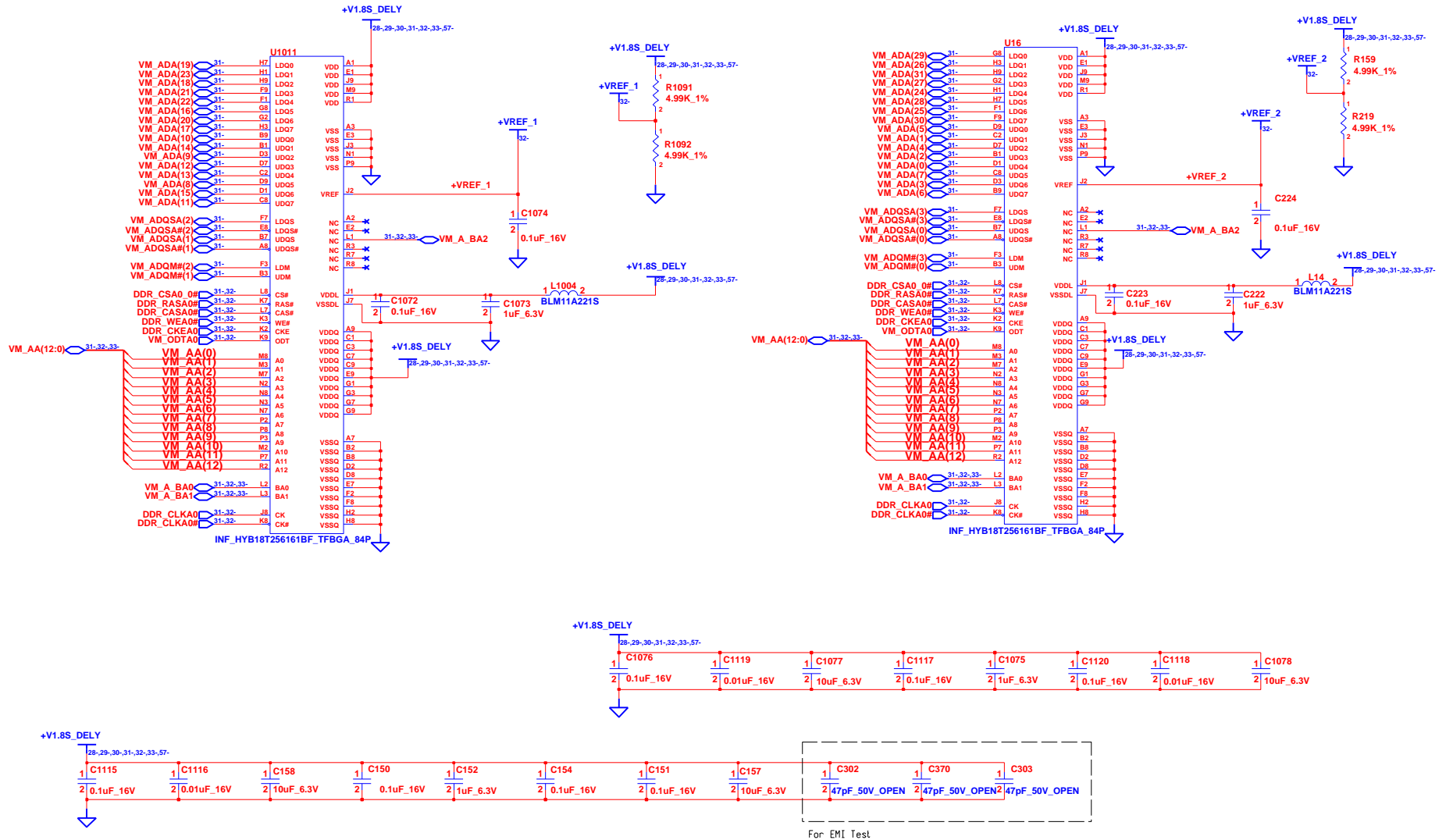
(Default)

POW_SW1	POW_SW0	+VDD_CORE
0	0	0.85V
0	1	0.9V
1	0	0.95V
1	1	1.0V





INVENTEC			
TITLE			
Piaget 2.0 Discrete			
GPU_AMD-M92S2-4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MTR	A02
SHEET		31	OF 60



For EMI Test

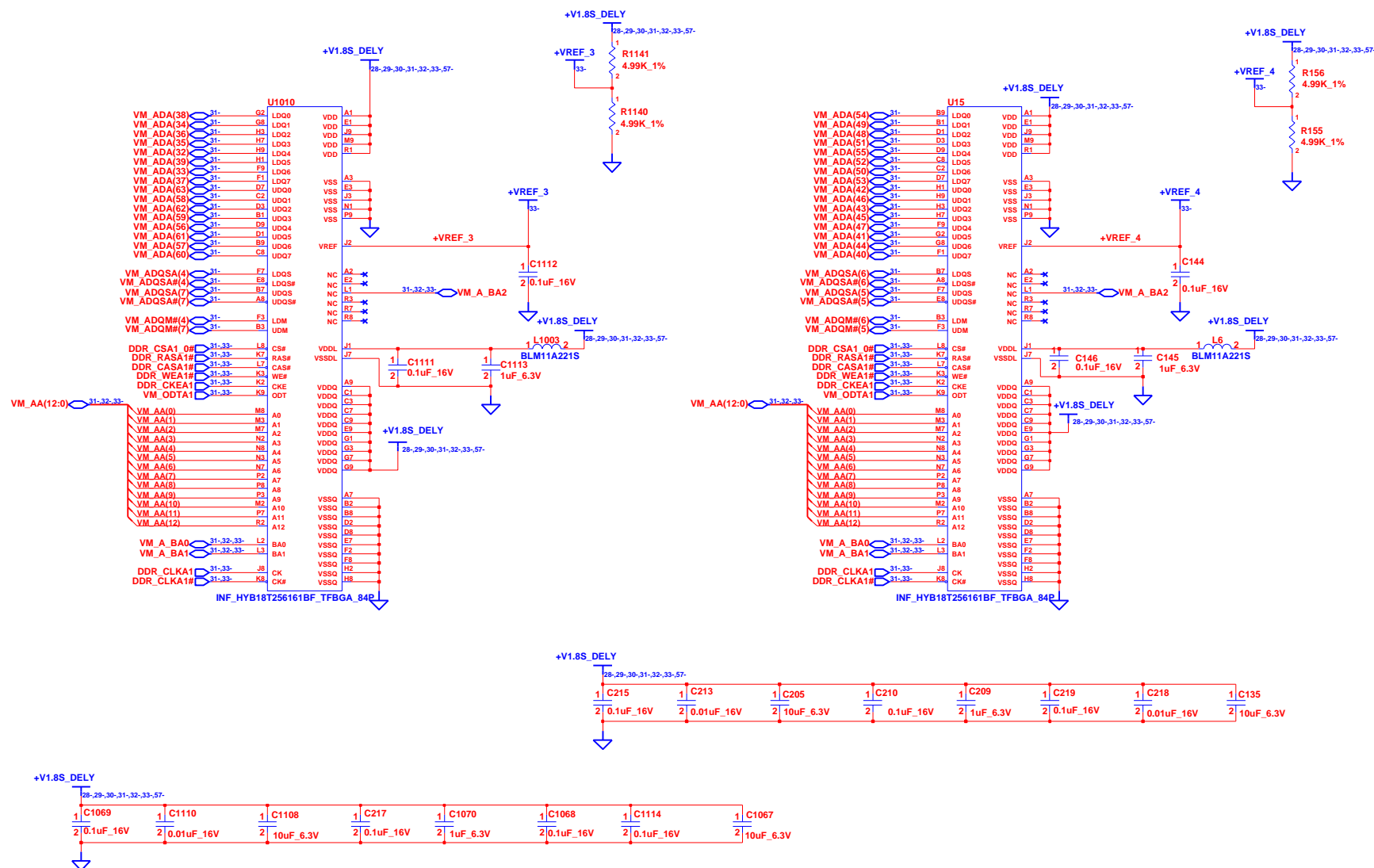
INVENTEC

TITLE
Piaget 2.0 Discrete
Video RAM (GDDR2) -1

SIZE CODE DOC. NUMBER REV
A3 CS 1310A22683-0-MT RA02

CHANGE by Jason Chiu 20-Jul-2009

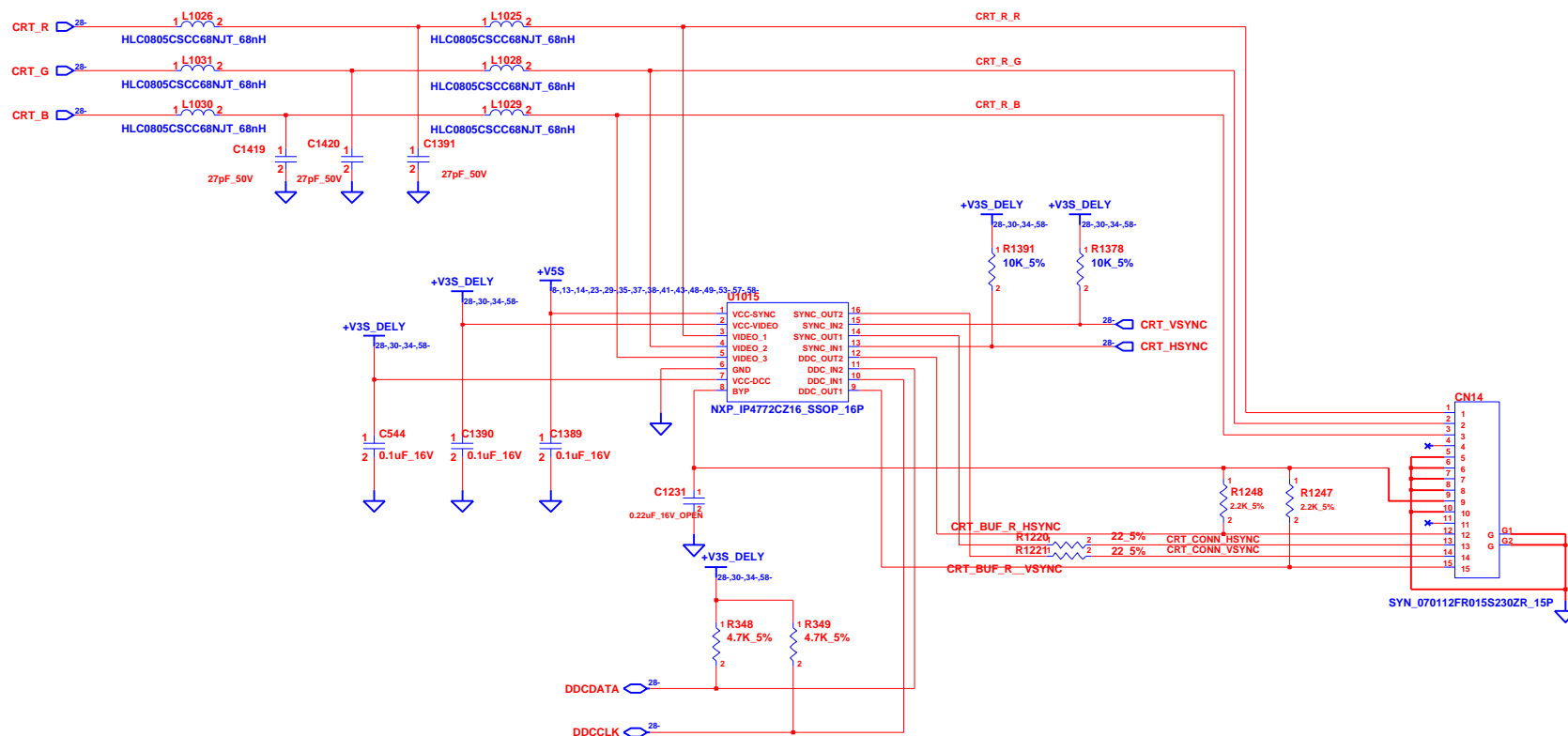
SHEET 32 OF 60



INVENTEC

TITLE			
Piaget 2.0 Discrete			
Video RAM (GDDR2)-2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22683-0-MT	RA02
SHEET		33	OF 60

CHANGE by Jason Chiu 20-Jul-2009



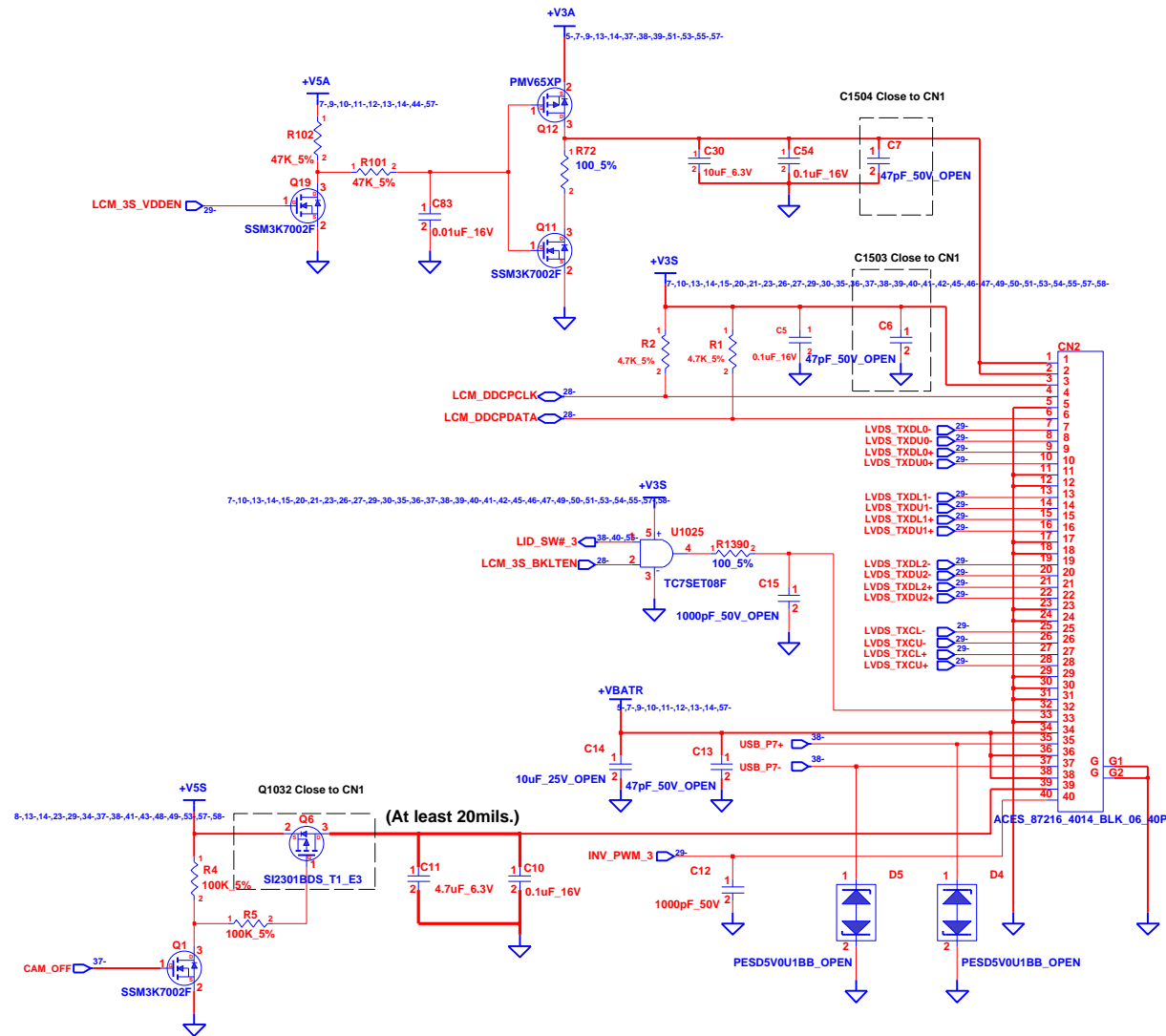
INVENTEC

TITLE
Piaget 2.0 Discrete CRT

SIZE CODE DOC. NUMBER REV
A3 CS 1310A22683-0-MTR A02

CHANGE by Jason Chiu 20-Jul-2009

SHEET 34 OF 60



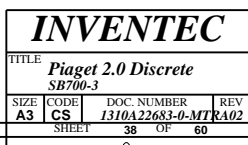
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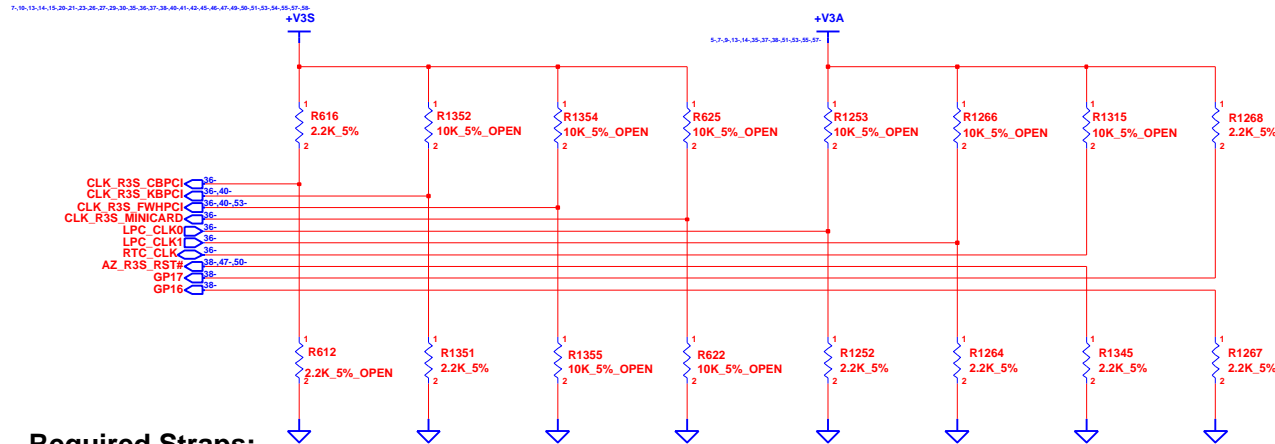
TITLE
**Piaget 2.0 Discrete
LCD CONN & WEBCAM CONN**

SIZE CODE DOC NUMBER REV
A3 CS 1310A22683-0-MTR A02

CHANGE by Jason Chiu 20-Jul-2009

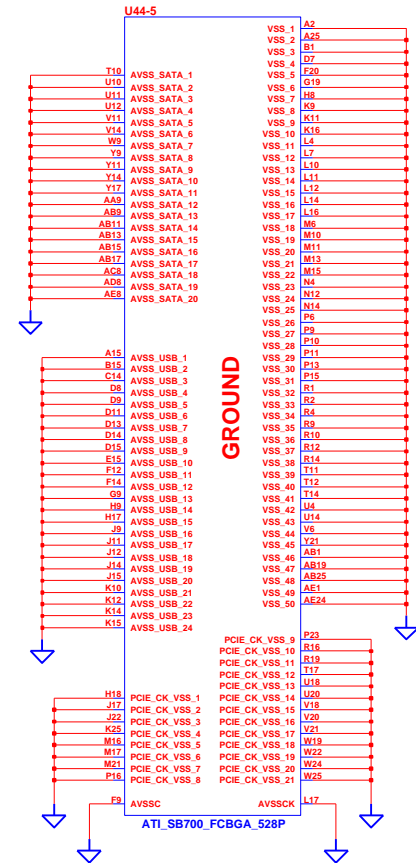
SHEET 35 OF 60





Required Straps:

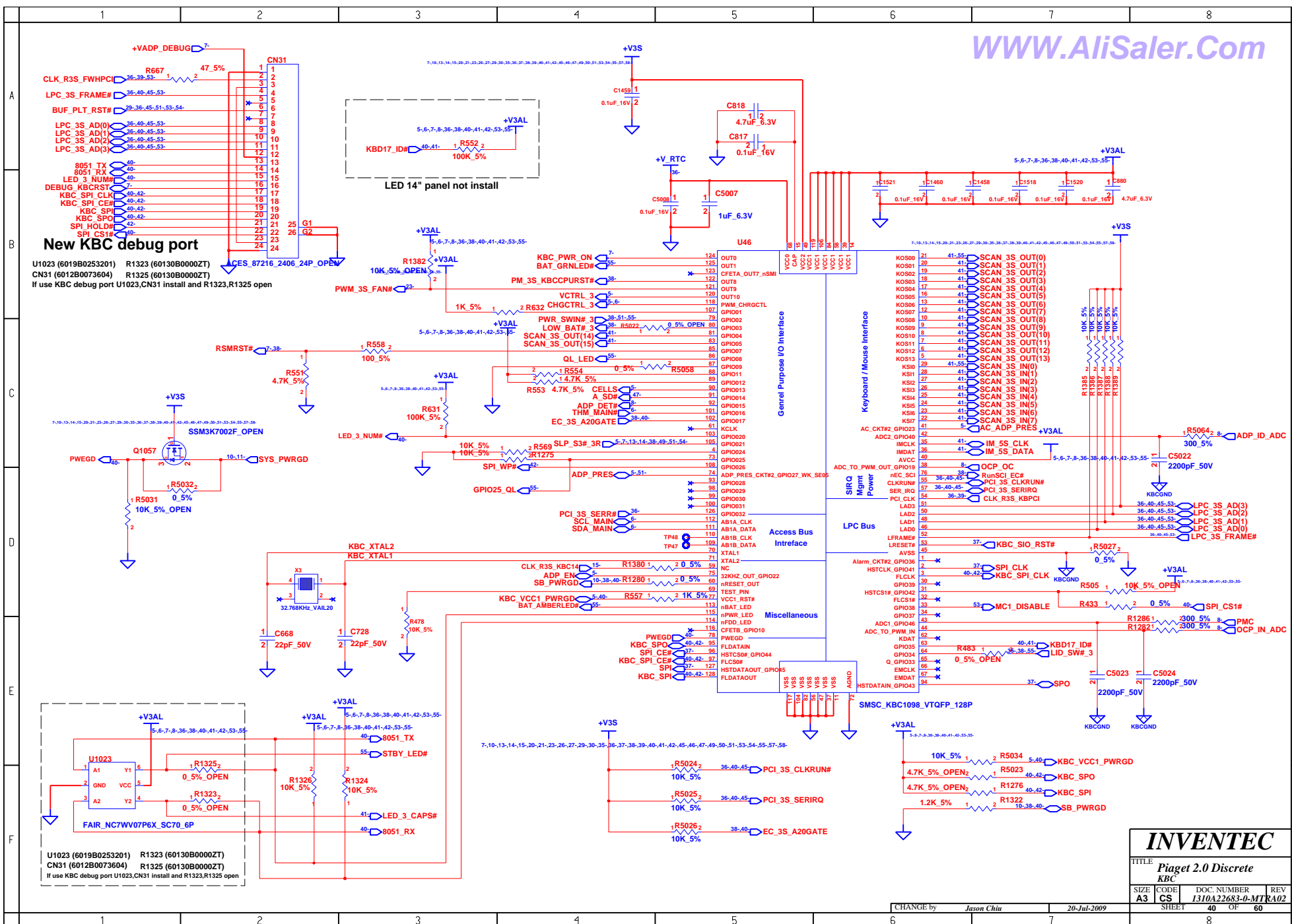
	CLK_R3S_CBP	CLK_R3S_KBP	CLK_R3S_FWH	CLK_R3S_MINICARD	LPC_CLK0	LPC_CLK1	RTC_CLK	AZ_RST#	GP17 GP16
PULL HIGH	BOOTFAIL TIMER ENABLED	USE DEBUG STRAPS	RESERVED	RESERVED	ENABLE PCI MEM BOOT	CLKGEN ENABLED	INTERNAL RTC DEFAULT	EC ENABLED	ROM TYPE H, H = Reserved H, L = SPI ROM
PULL LOW	BOOTFAIL TIMER DISABLED DEFAULT	IGNORE DEBUG STRAPS DEFAULT			DISABLE PCI MEM BOOT DEFAULT	CLKGEN DISABLED DEFAULT	EXT. RTC (PD on X1, apply 32KHz to RTC_CLK)	EC DISABLED DEFAULT	L, H = LPC ROM L, L = FWH ROM

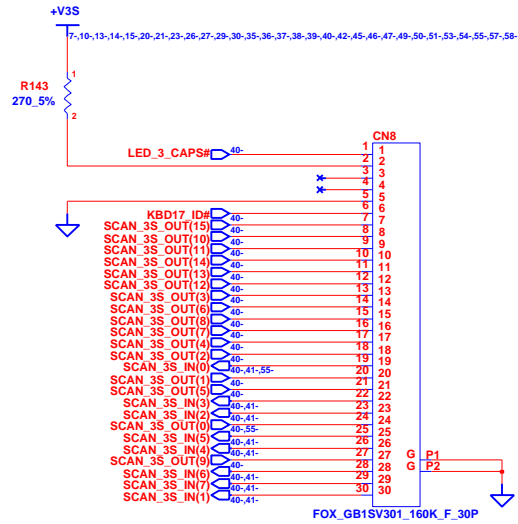


INVENTEC

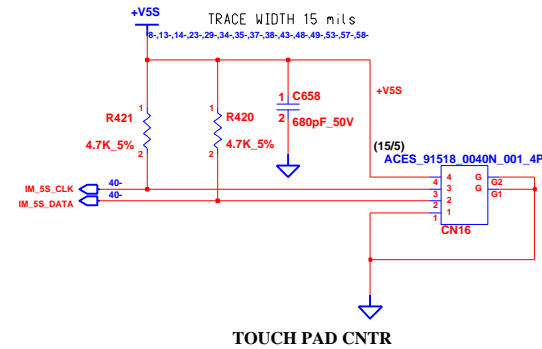
TITLE Piaget 2.0 Discrete SB700-4			
SIZE A3	CODE CS	DOC. NUMBER 1310A22683-0-MTR	REV A02
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CHANGE by Jason Chiu 20-Jul-2009

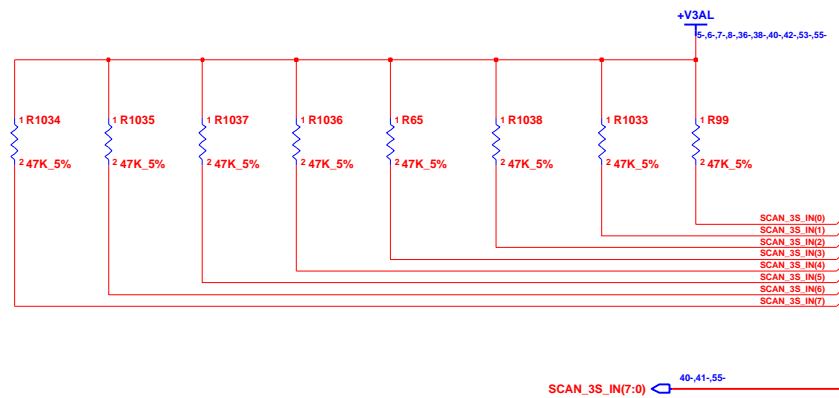




KEYBOARD CONN

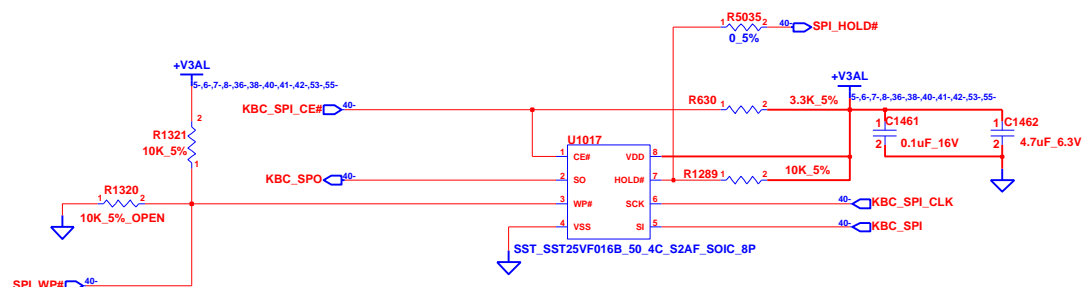


TOUCH PAD CNTR

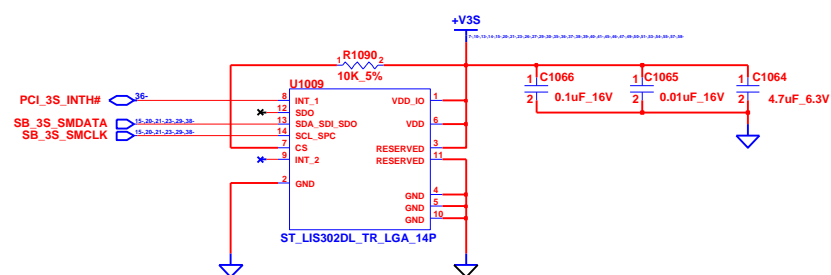


SCAN_3S_IN(7:0)

INVENTEC			
TITLE: Piaget 2.0 Discrete KB&TP CONN			
SIZE: A3	CODE: CS	DOC. NUMBER: 1310A22683-0-MTR	REV: A02
CHANGE by: Jason Chiu		20-Jul-2009	
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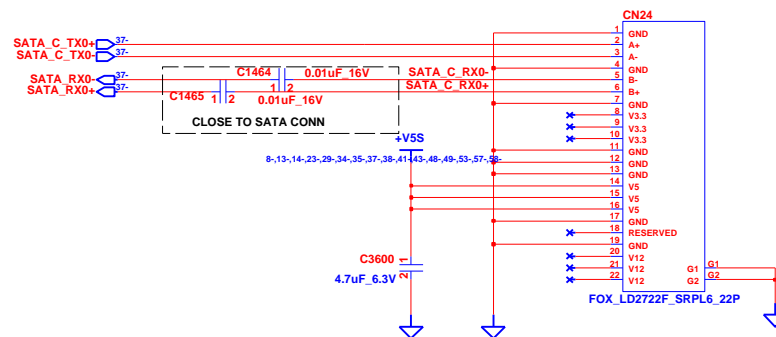


SPI Flash

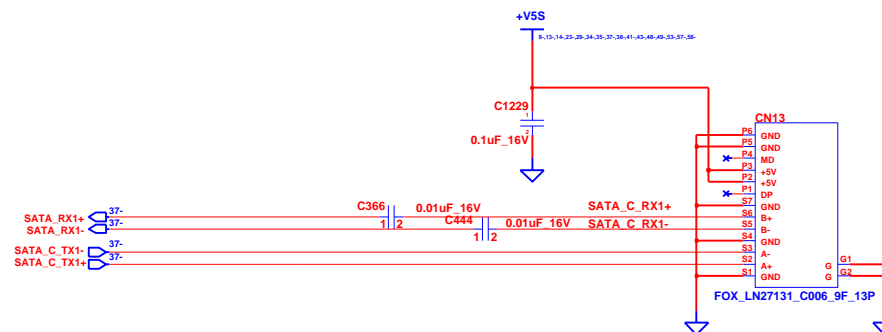


Accelerometer

INVENTEC			
TITLE: Piaget 2.0 Discrete SPI & ACCELEROMETER			
SIZE: A3	CODE: CS	DOC. NUMBER: I310A22683-0-MTR	REV: A02
CHANGE by: Jason Chin		20-Jul-2009	
SHEET: 42		OF: 60	

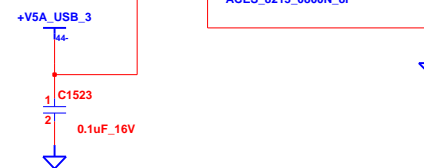
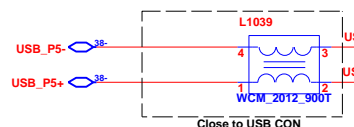
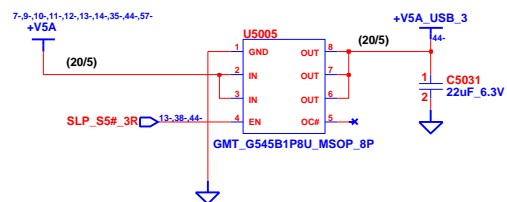
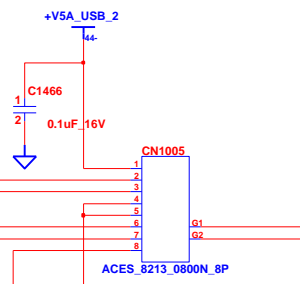
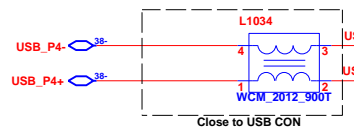
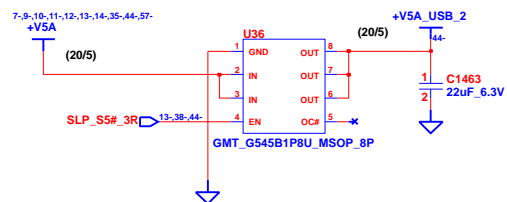
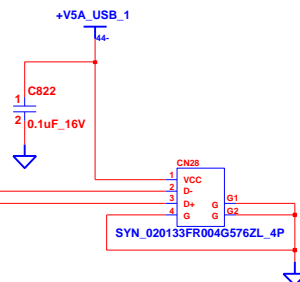
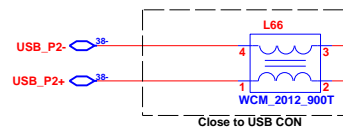
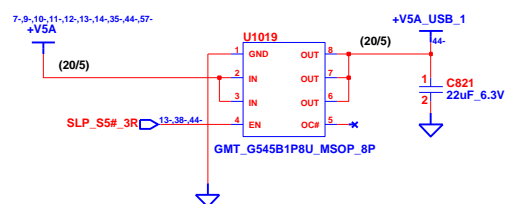
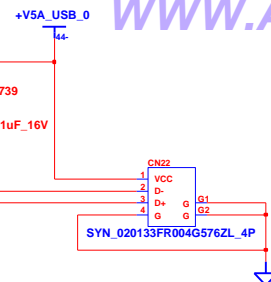
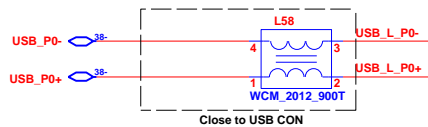
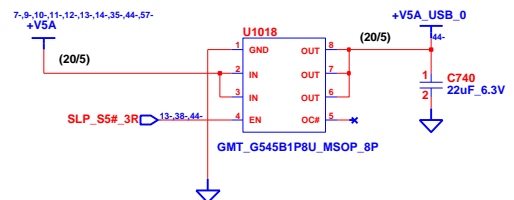


HDD CONNECTOR



ODD SATA CONNECTOR

INVENTEC			
TITLE Piaget 2.0 Discrete HDD & ODD CONN			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR A02	REV 1
CHANGE by Jason Chiu		20-Jul-2009	SHEET 43 OF 60



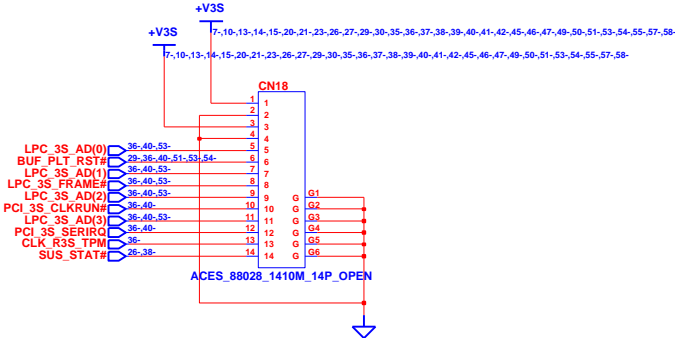
INVENTEC

TITLE
Piaget 2.0 Discrete
USB CONN

SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
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CHANGE by Jason Chiu 20-Jul-2009

SHEET 44 OF 60



INVENTEC			
TITLE Piaget 2.0 Discrete TCM CONN			
SIZE A3	CODE CS	DOC. NUMBER 1310A22683-0-MTR	REV A02
SHEET 45		OF 60	

CHANGE by Jason Chin 20-Jul-2009

4 IN 1 Card Reader
(SD/MMC/MS/xD)

INVENTEC
Piaget 2.0 Discrete
FLASH MEDIA CARD

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MT	RA02

CHANGE by: Jason Chiu 20-Jul-2009

SHEET 46 OF 60

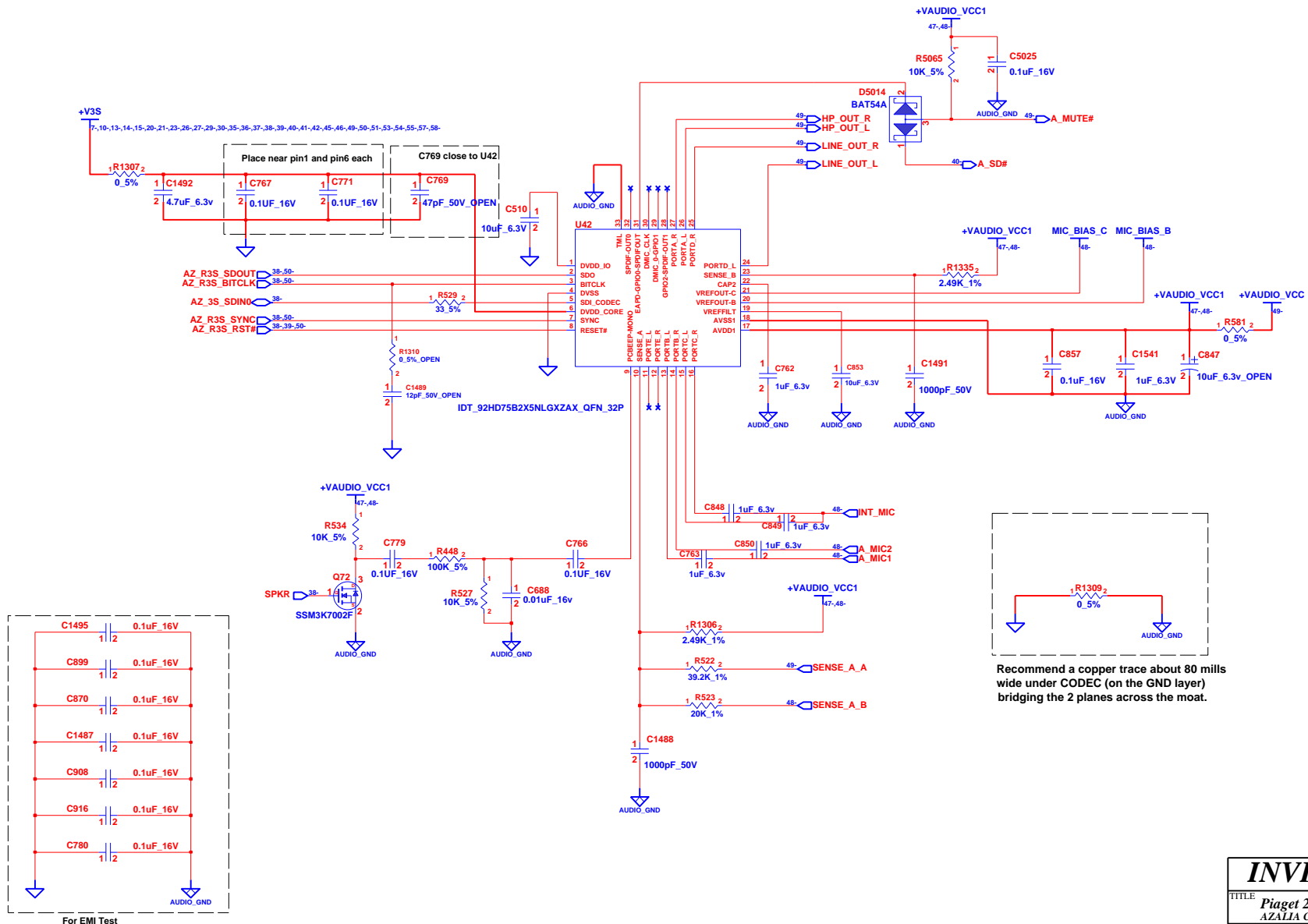
4 IN 1 Card Reader
(SD/MMC/MS/xD)

INVENTEC
Piaget 2.0 Discrete
FLASH MEDIA CARD

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MT	RA02

CHANGE by: Jason Chiu 20-Jul-2009

SHEET 46 OF 60



INVENTEC

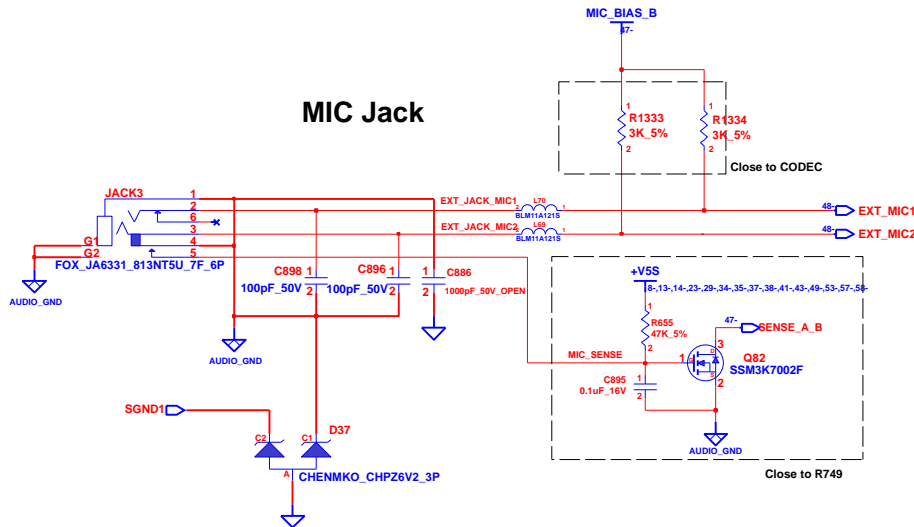
TITLE
**Piaget 2.0 Discrete
AZALIA CODEC**

SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV RA02
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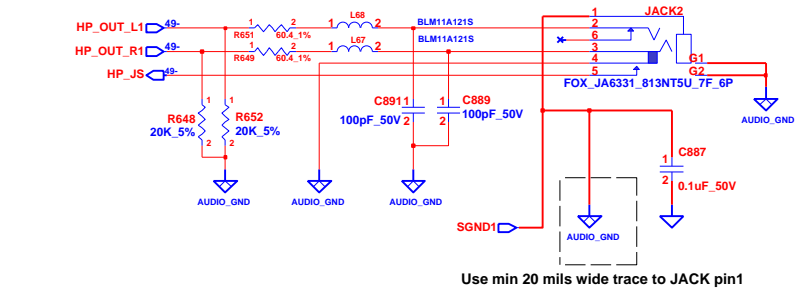
CHANGE by *Jason Chiu* 20-Jul-2009

SHEET 47 OF 60

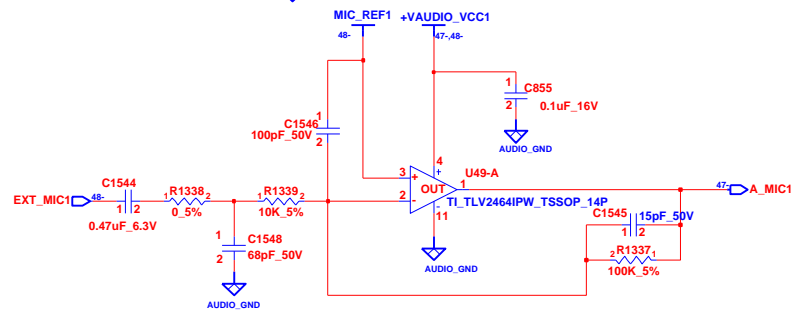
MIC Jack



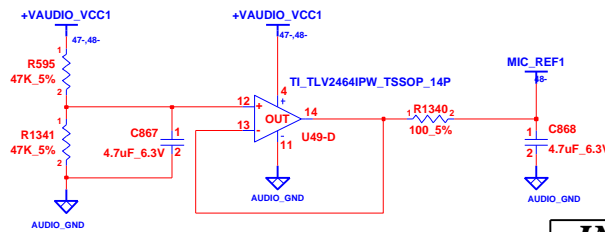
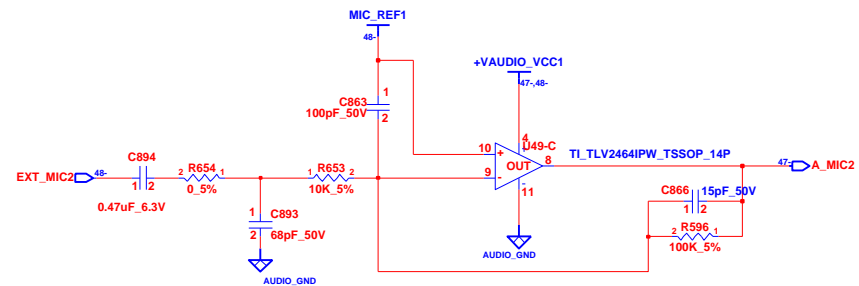
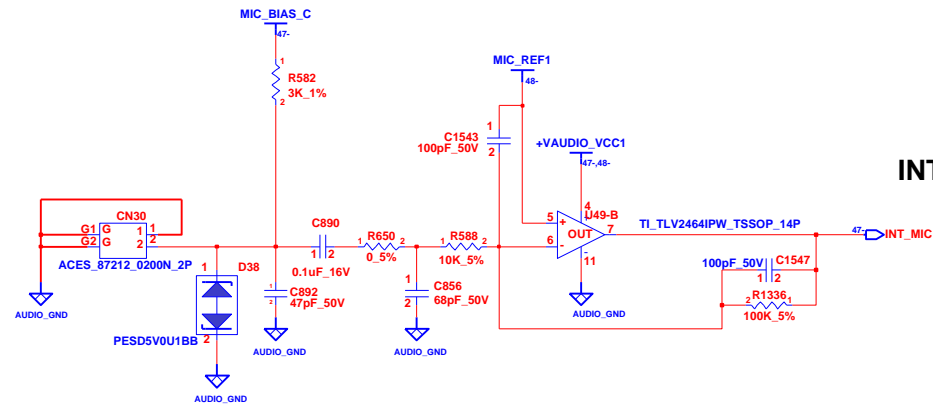
Earphone Jack



EXT. MIC

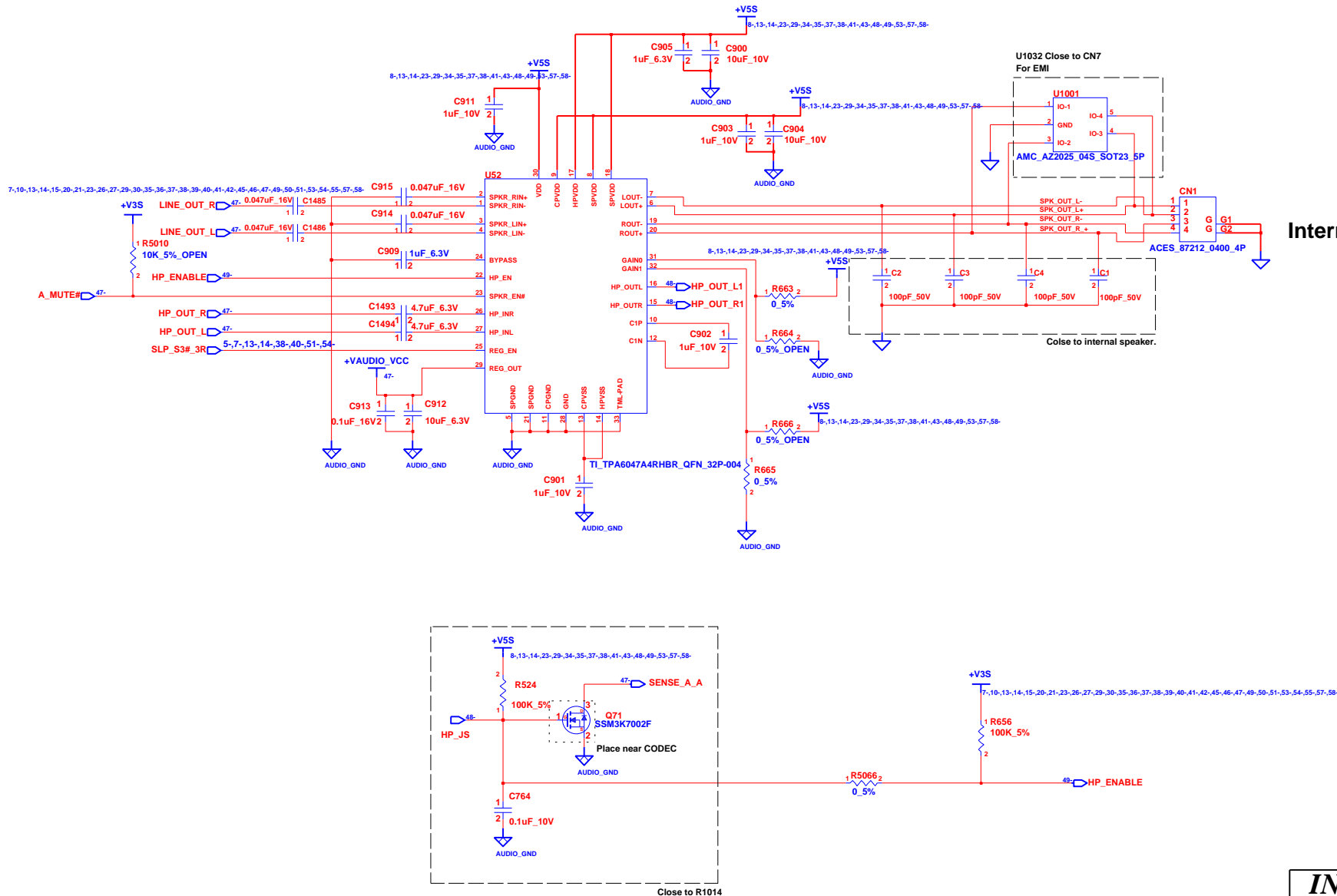


INT. MIC



INVENTEC

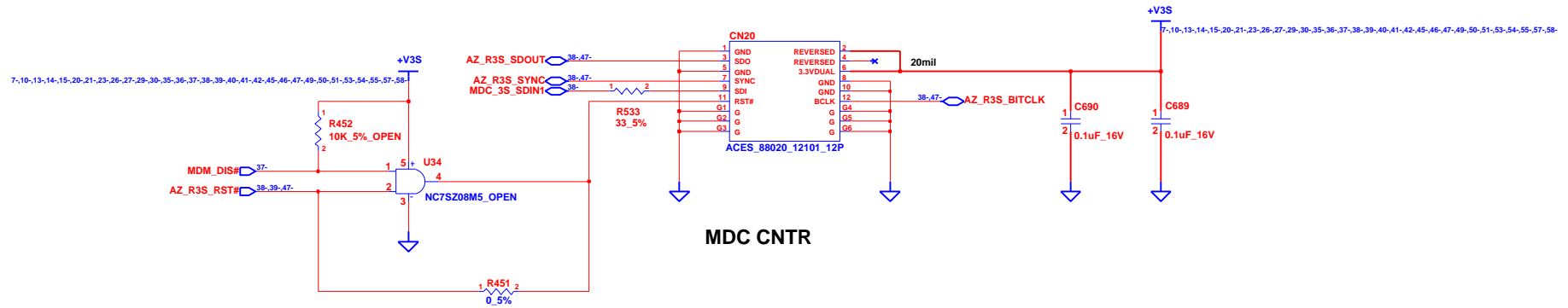
TITLE Piaget 2.0 Discrete Earphone & MIC JACK			
SIZE A3	CODE CS	DOC. NUMBER 1310A22683-0-MT	REV A02
CHANGE by Jason Chiu		20-Jul-2009	
SHEET 48		OF 60	



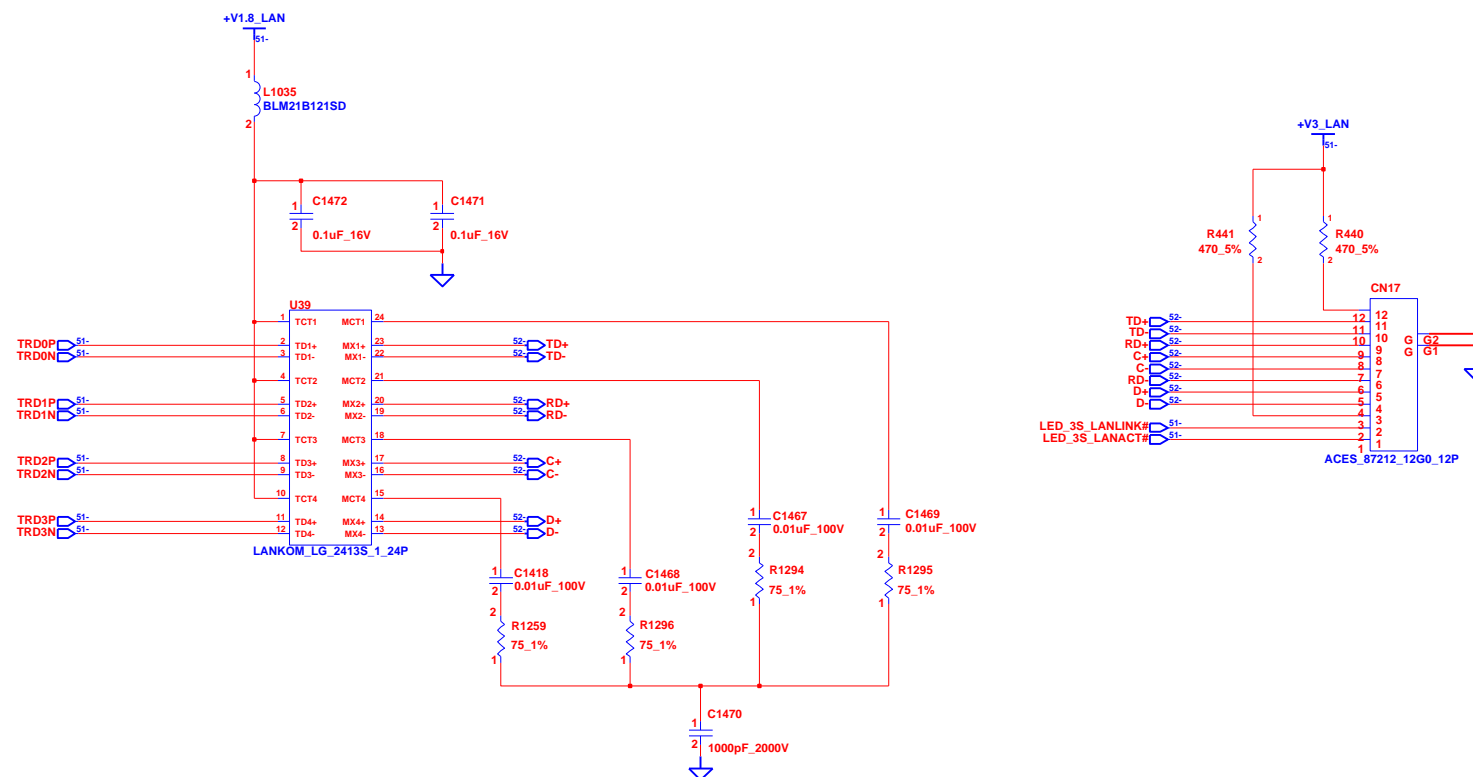
Internal Speaker

INVENTEC

TITLE			
Piaget 2.0 Discrete AUDIO AMP & HP JACK			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22683-0-MTR	RA02
CHANGE by		Jason Chiu	20-Jul-2009
SHEET		49	OF 60



INVENTEC			
TITLE: <i>Piaget 2.0 Discrete</i>			
MDC CNTR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MTR	RA02
CHANGE by: Jason Chiu		20-Jul-2009	
SHEET		50	OF 60



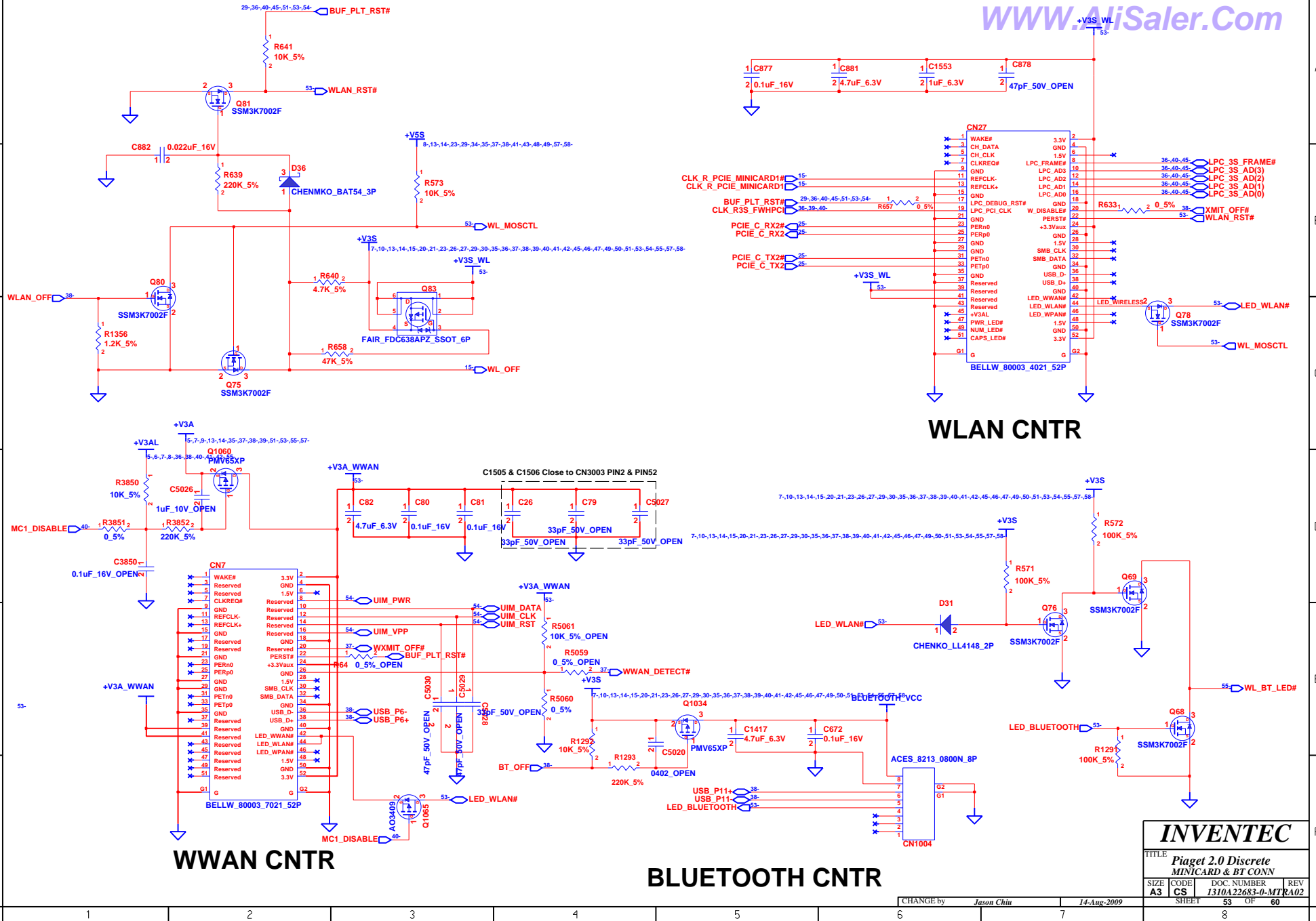
INVENTEC

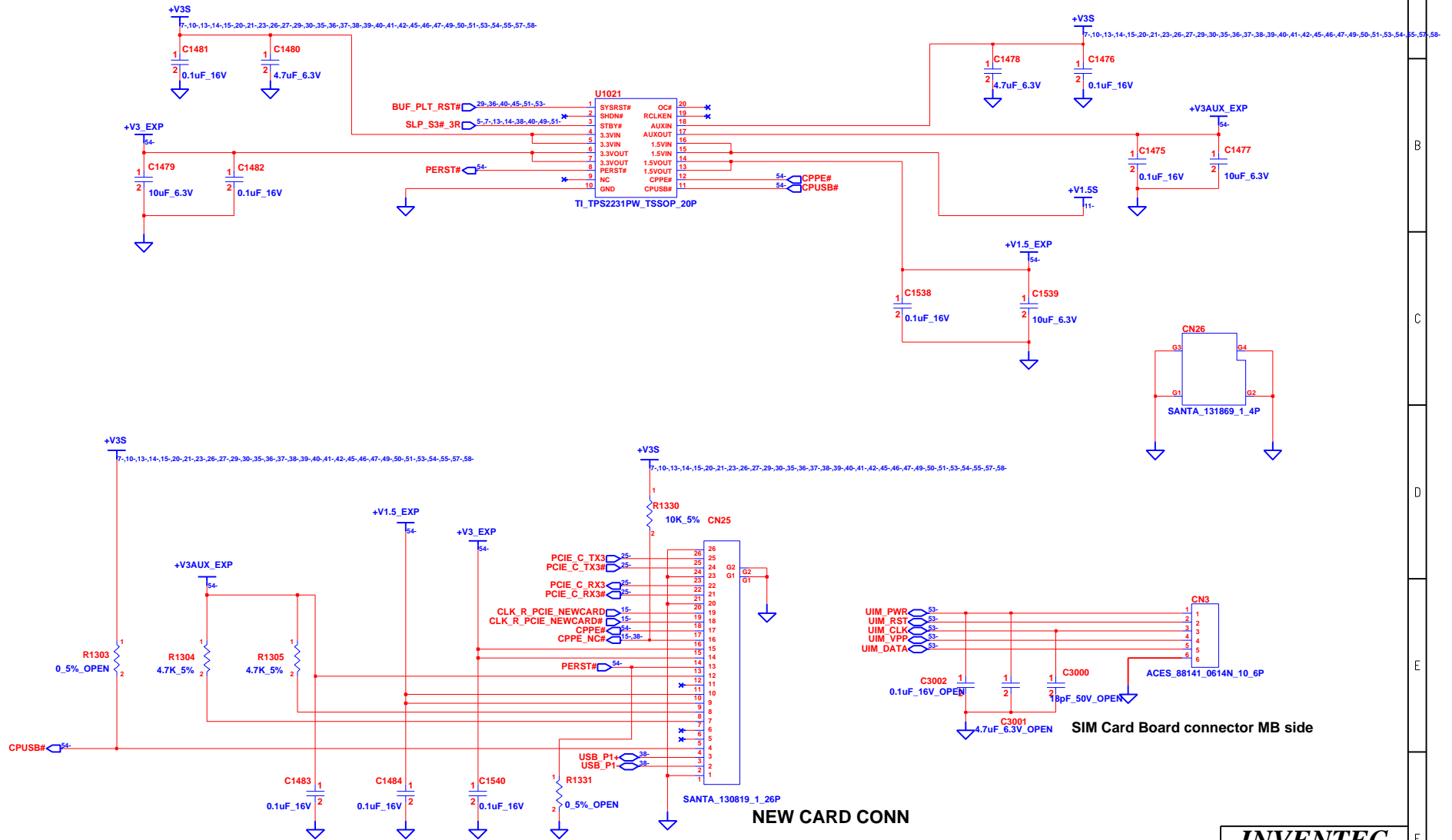
TITLE
Piaget 2.0 Discrete
NIC 10/100/1000-RJ45 CONN

SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV RA02
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CHANGE by *Jason Chiu* 20-Jul-2009

SHEET 52 OF 60

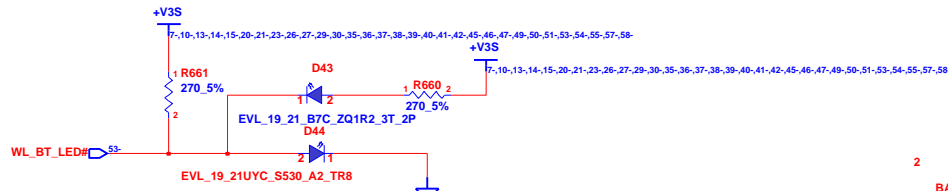




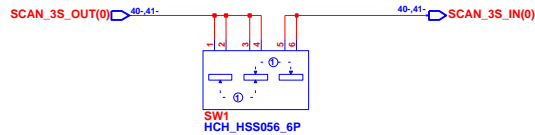
INVENTEC

TITLE Piaget 2.0 Discrete NEW CARD & SIM CONN			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
SHEET		54	OF 60

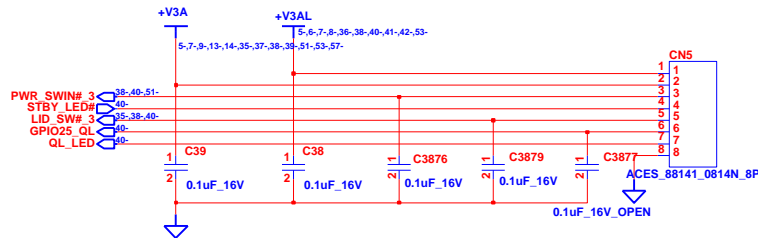
CHANGE by Jason Chiu 20-Jul-2009



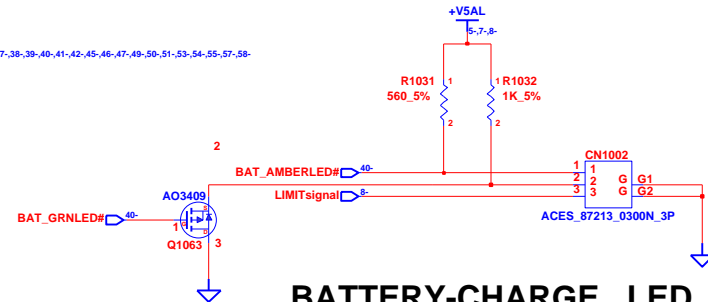
WIRELESS LED



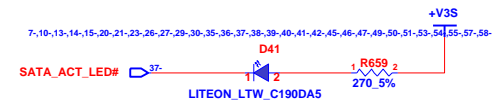
WIRELESS BUTTON



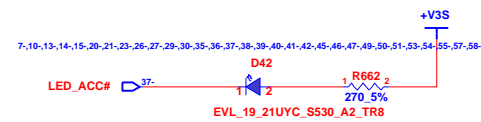
Swth Board & LED Board CONN



BATTERY-CHARGE LED



HDD & ODD LED



ACCELEROMTER LED

INVENTEC			
TITLE: Piaget 2.0 Discrete W/B & SW/B & LED/B CONN			
SIZE: A3	CODE: CS	DOC. NUMBER: I310A22683-0-MTR	REV: A02
CHANGE by: Jason Chiu		20-Jul-2009	
SHEET: 55		OF: 60	

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F	<div> <div>MAIN BOARD</div> <div>CPU</div> <div>MINI CARD</div> <div>GPU</div> <div>MDC</div> </div> <div> <div>FIX1004</div> <div>FIX1001</div> <div>FIX3</div> <div>FIX8</div> <div>FIX2</div> <div>FIX7</div> <div>FIX1003</div> <div>FIX1002</div> </div>								F
	1	2	3	4	5	6	7	8	

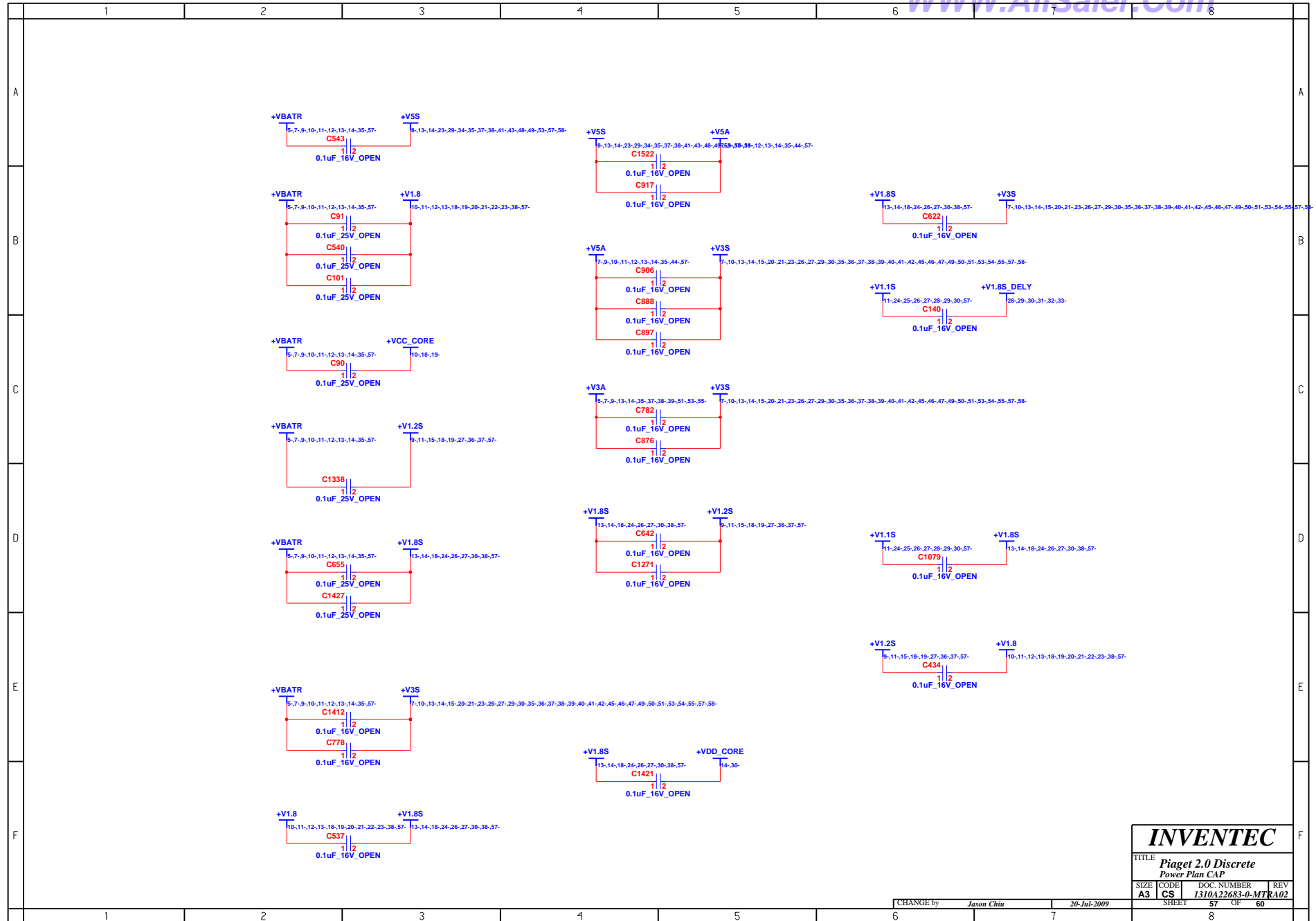
INVENTEC

TITLE **Piaget 2.0 Discrete SCREW**

SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
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CHANGE by *Jason Chin* 20-Jul-2009

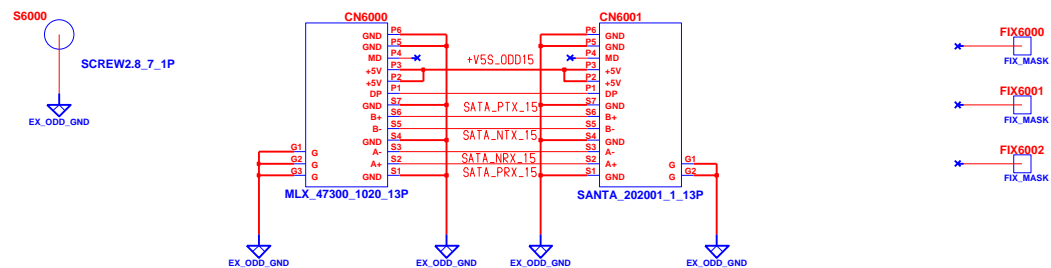
SHEET 56 OF 60



INVENTEC			
TITLE Piaget 2.0 Discrete Power Plan CAP			
SIZE A3	CODE CS	DOC NUMBER I310A22683-0-MTRA02	REV
CHANGE by Jason Chiu		20-Jul-2009	
SHEET 57		OF 60	

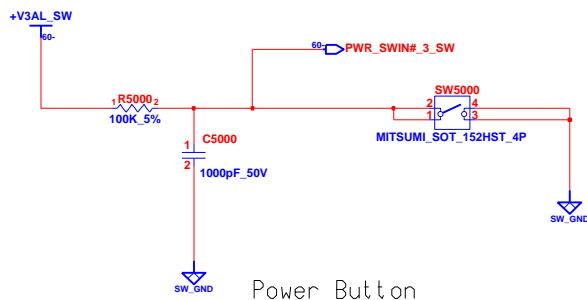
WWW.AliSaler.Com

WWW.AliSaler.Com

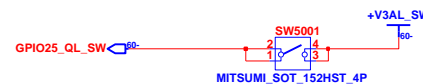


15'' ODD Extend Board

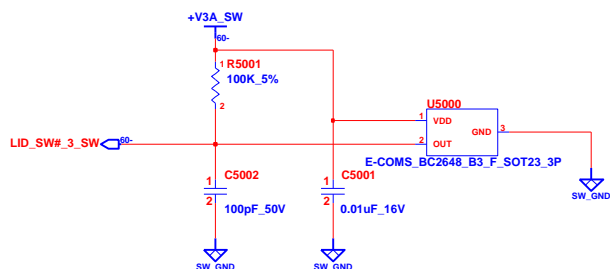
INVENTEC			
TITLE Piaget 2.0 Discrete 15'' ODD Extend Board			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
CHANGE by Jason Chiu		20-Jul-2009	
SHEET		59	OF 60



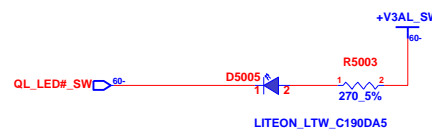
Power Button



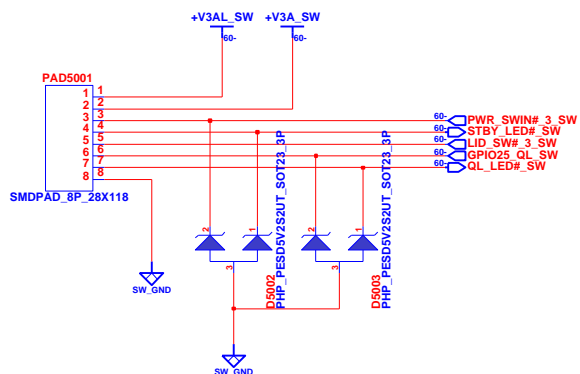
Switch & LED Board



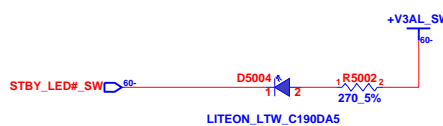
LID Switch



Quick Look LED



Switch & LED Board



Power/Standby LED

- FIX5000
- FIX5001
- FIX5002
- FIX5003
- FIX5004
- FIX5005

INVENTEC

TITLE		
Piaget 2.0 Discrete		
15" ODD Extend Board		
SIZE	CODE	DOC. NUMBER
A3	CS	1310A22683-0-MTR
SHEET		REV
60		RA02

CHANGE by Jason Chiu 20-Jul-2009

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